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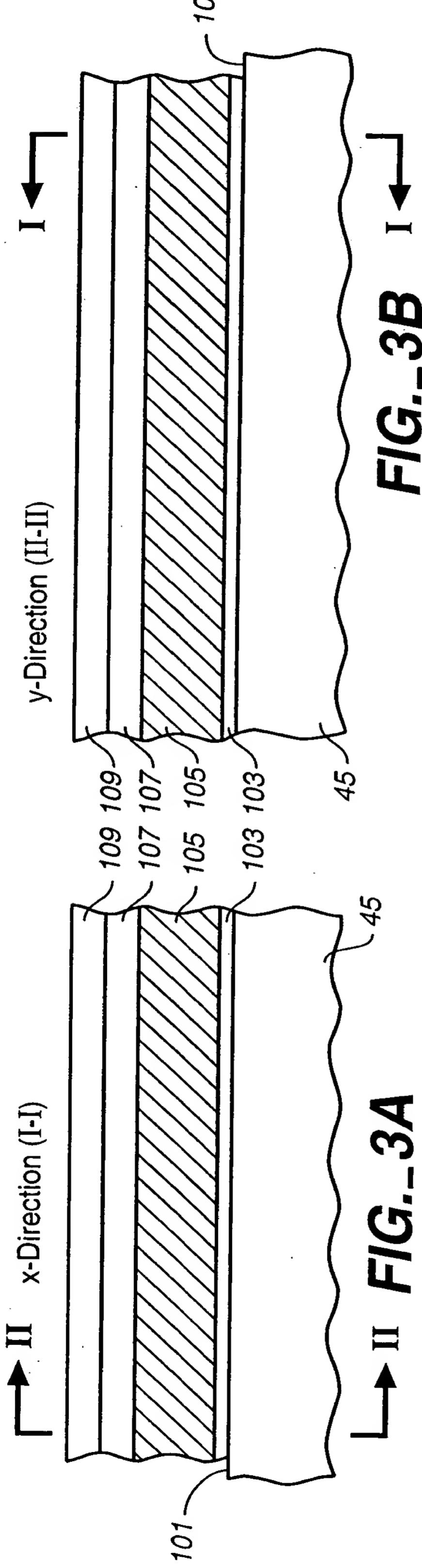


FIG._3A FIG._3B

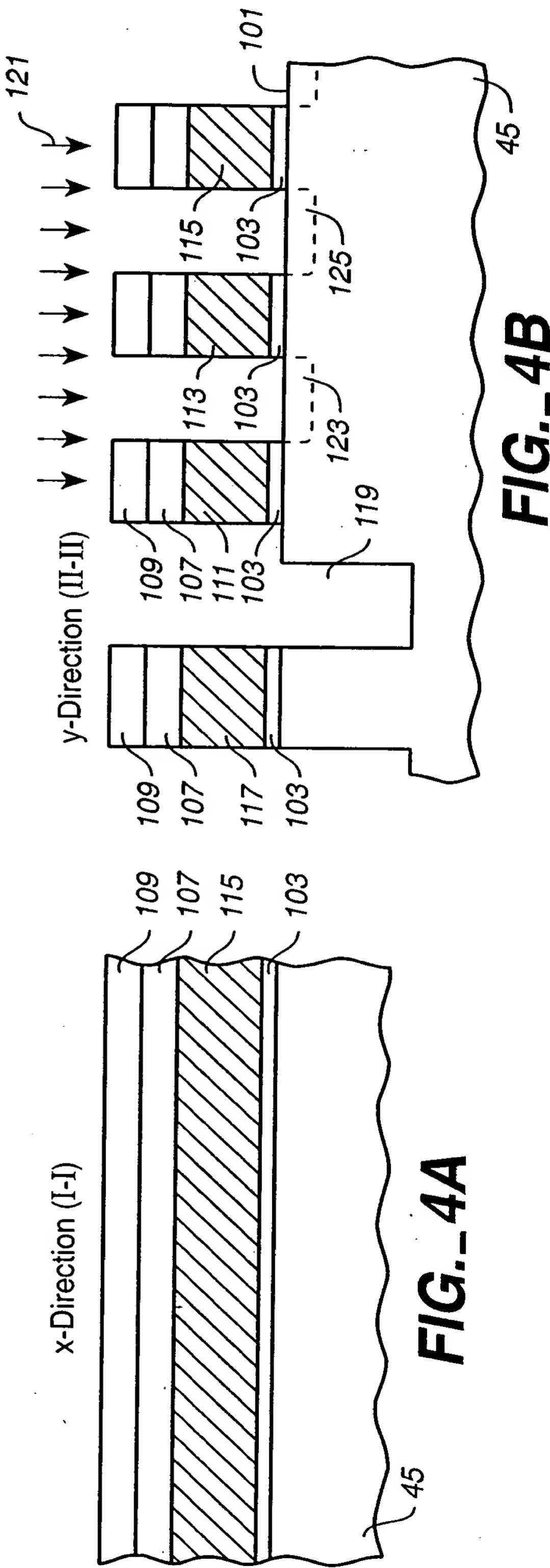


FIG._4A FIG._4B

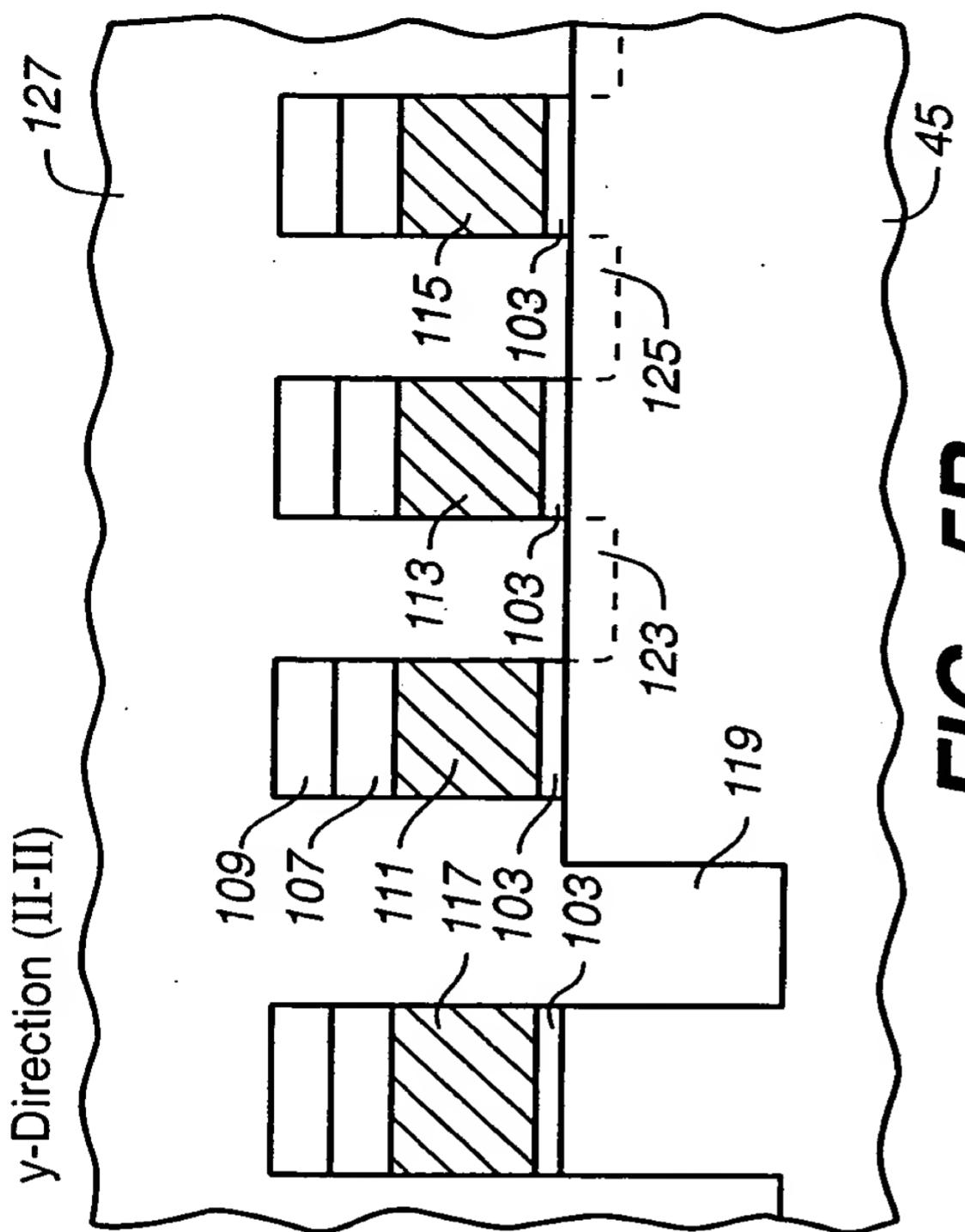


FIG._5B

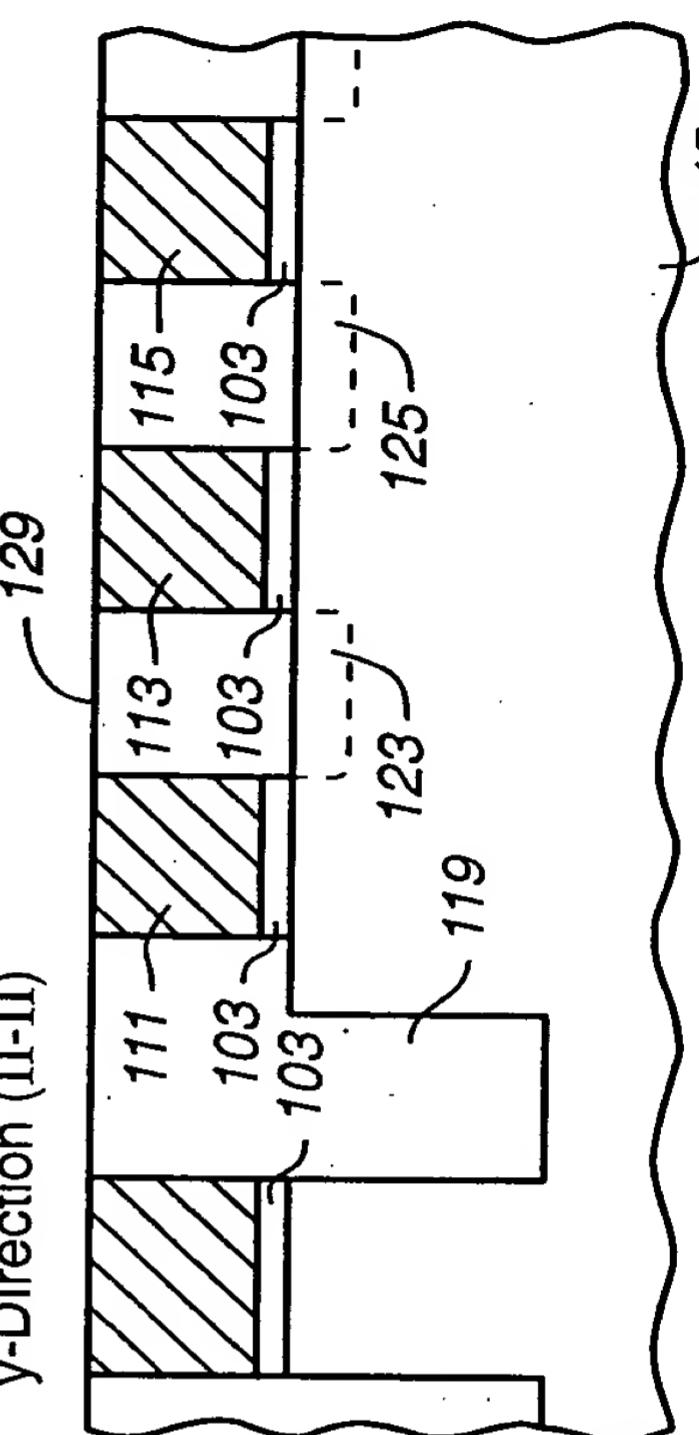


FIG._6B

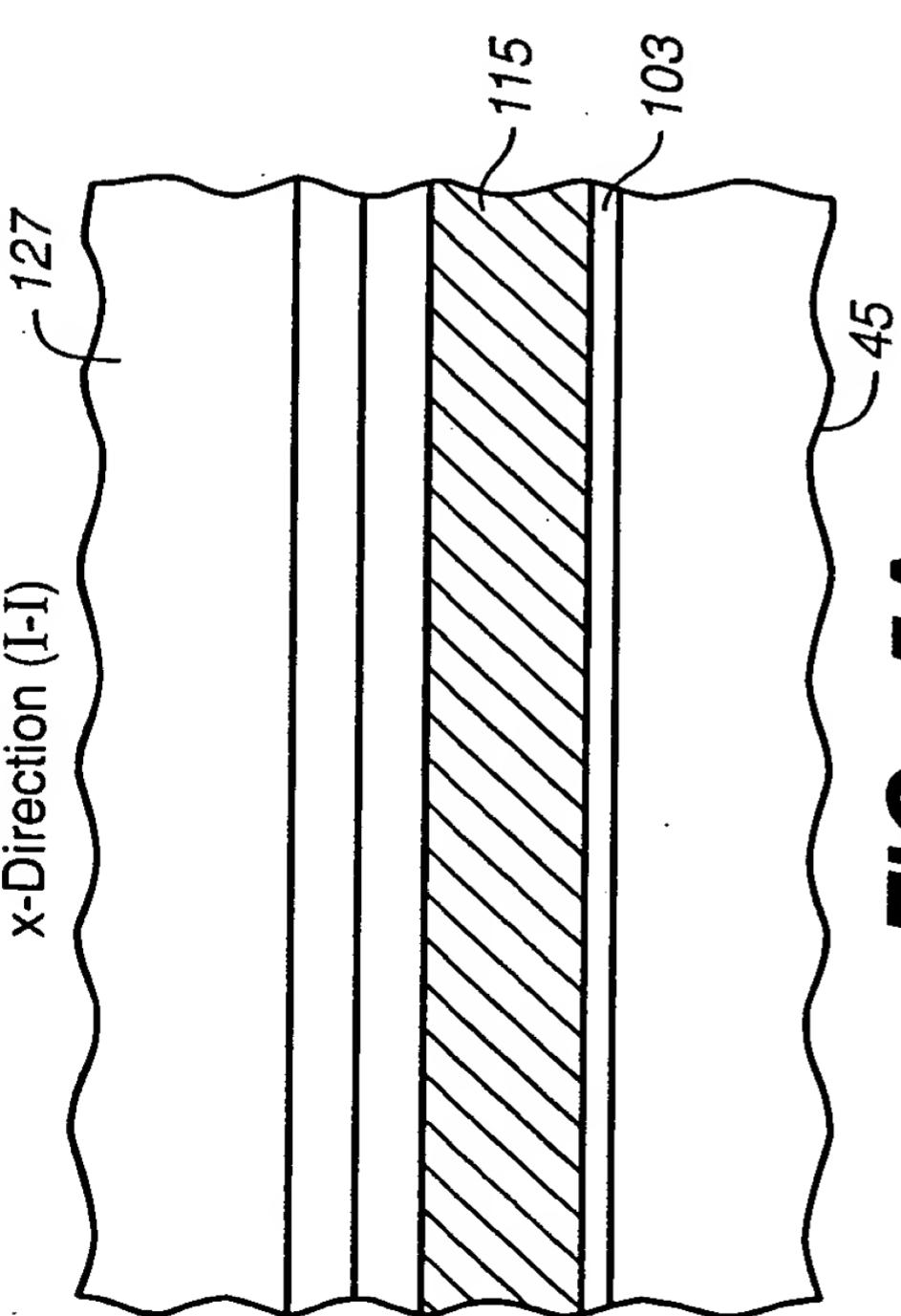


FIG._5A

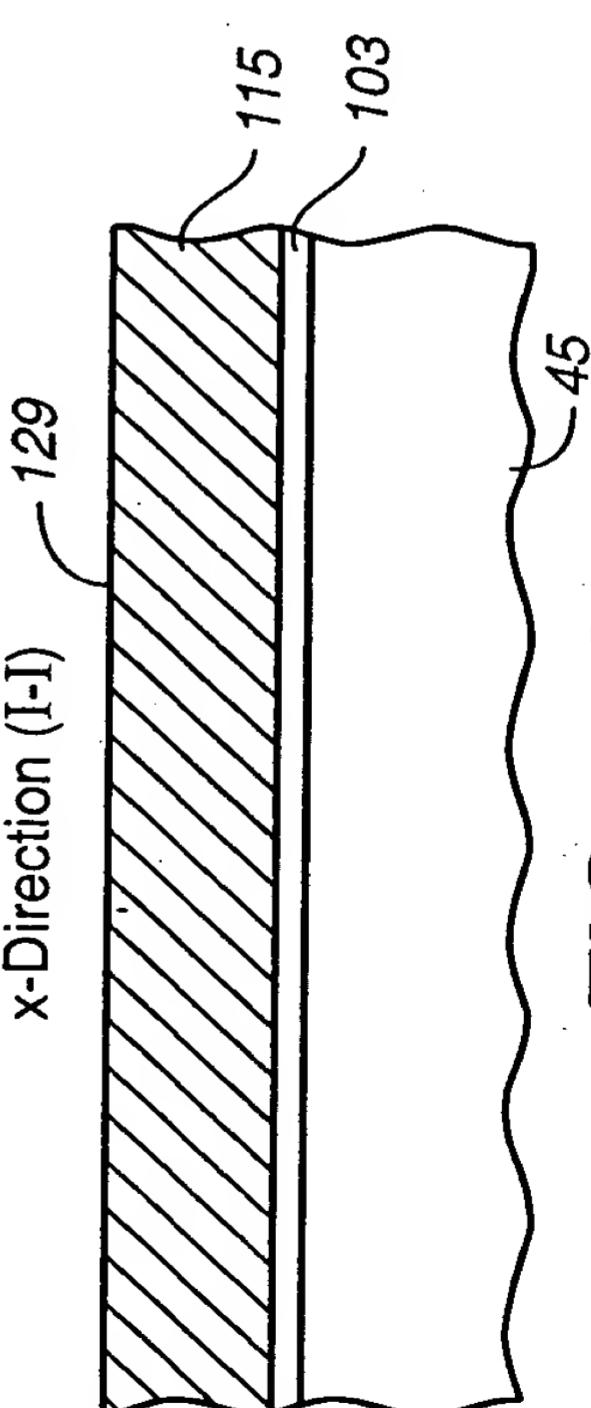


FIG._6A



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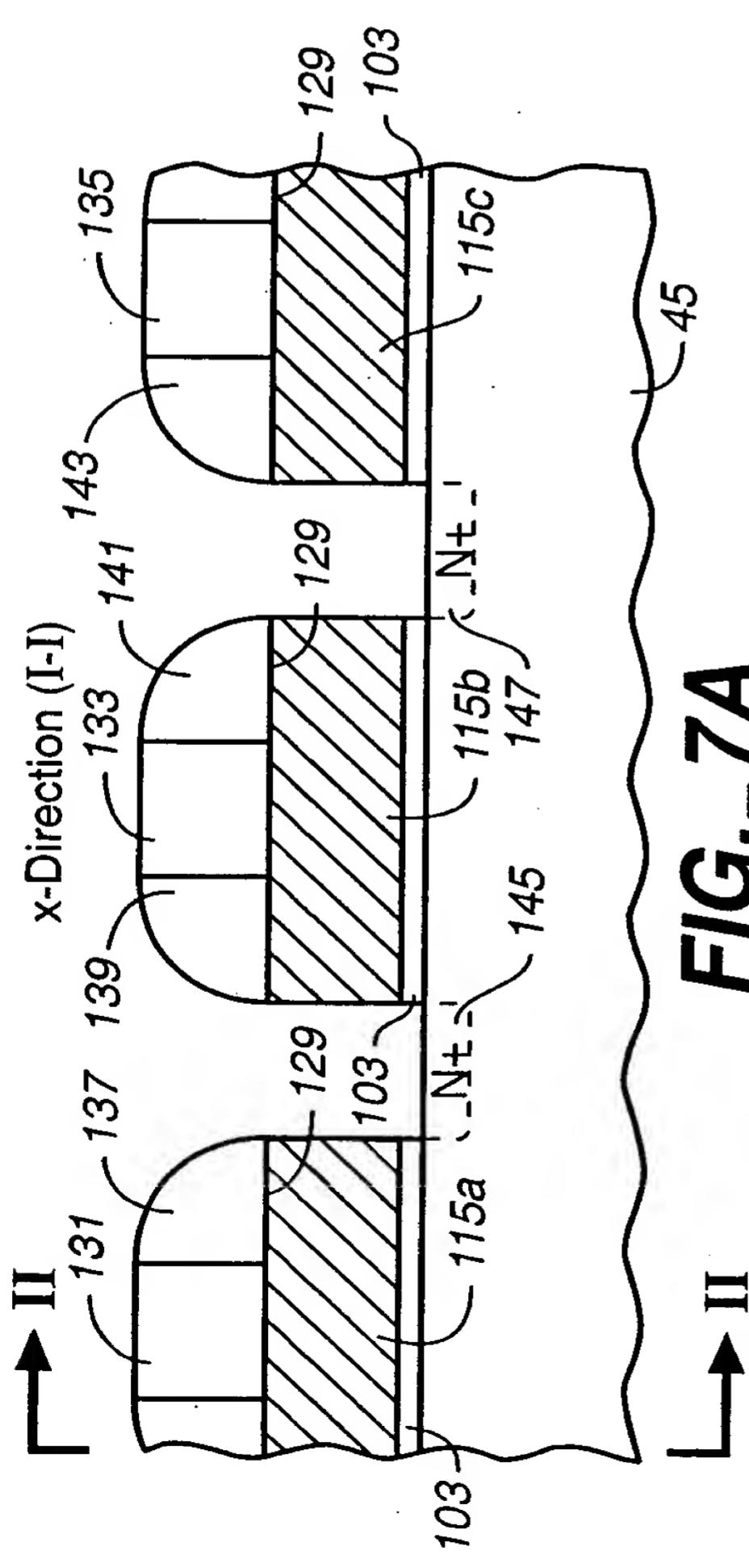


FIG. 7A

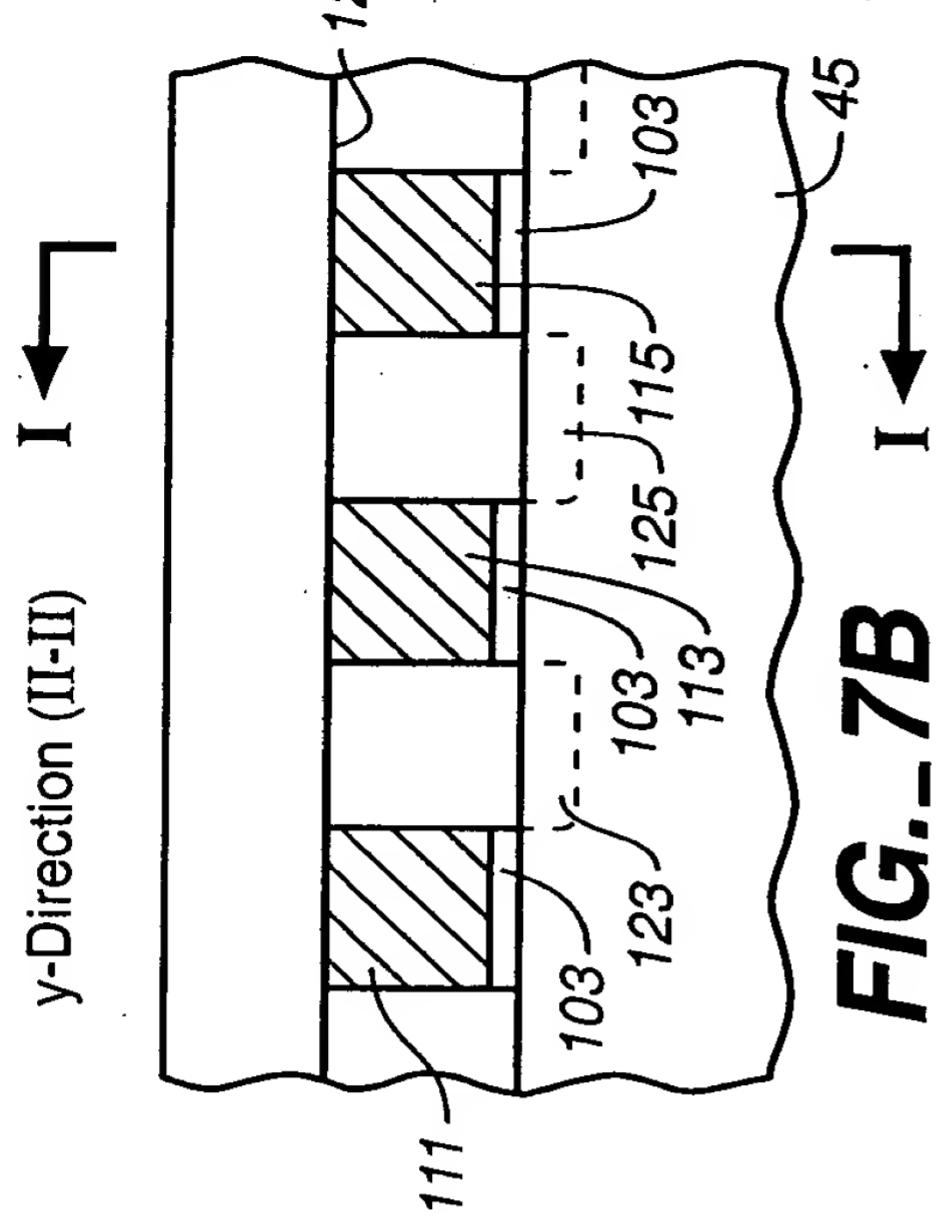


FIG. 7B

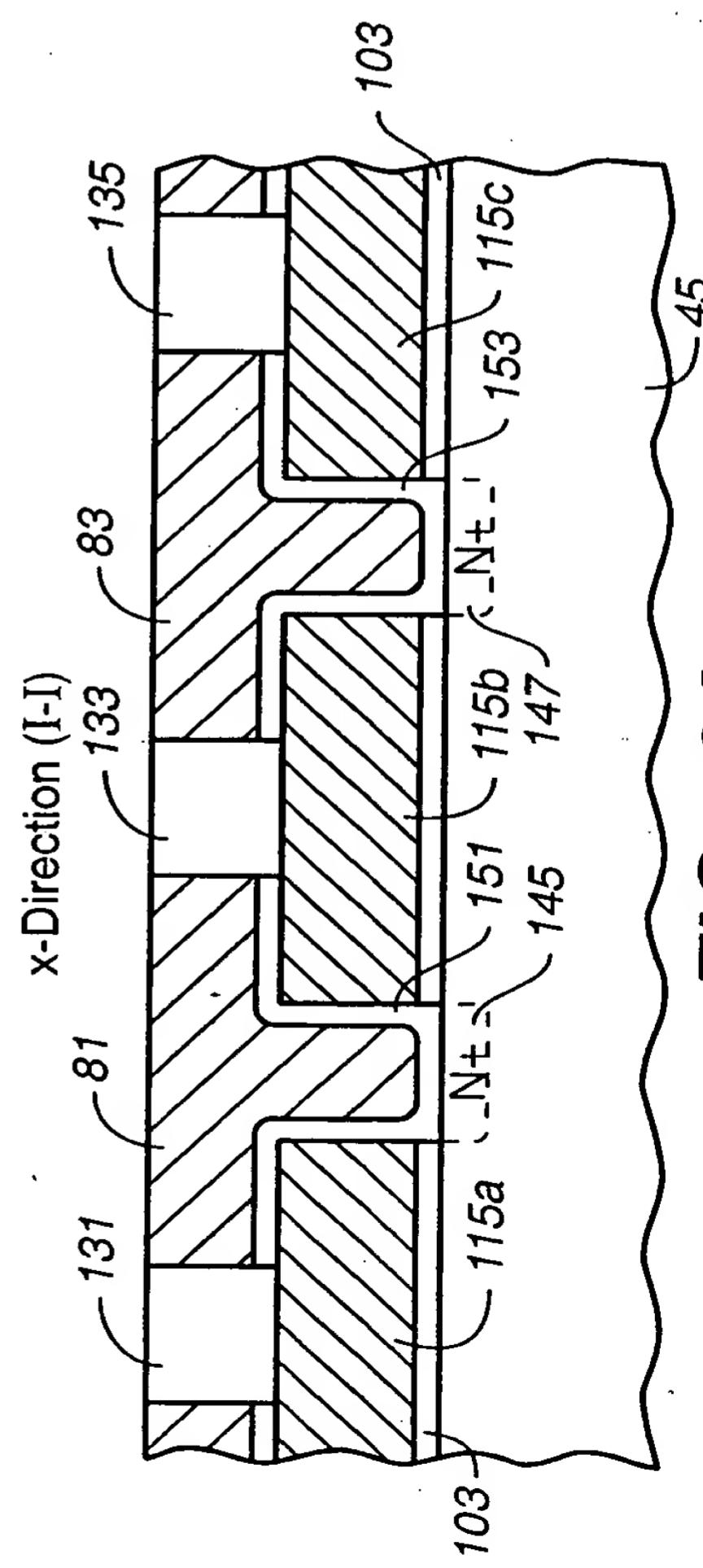


FIG. 8A

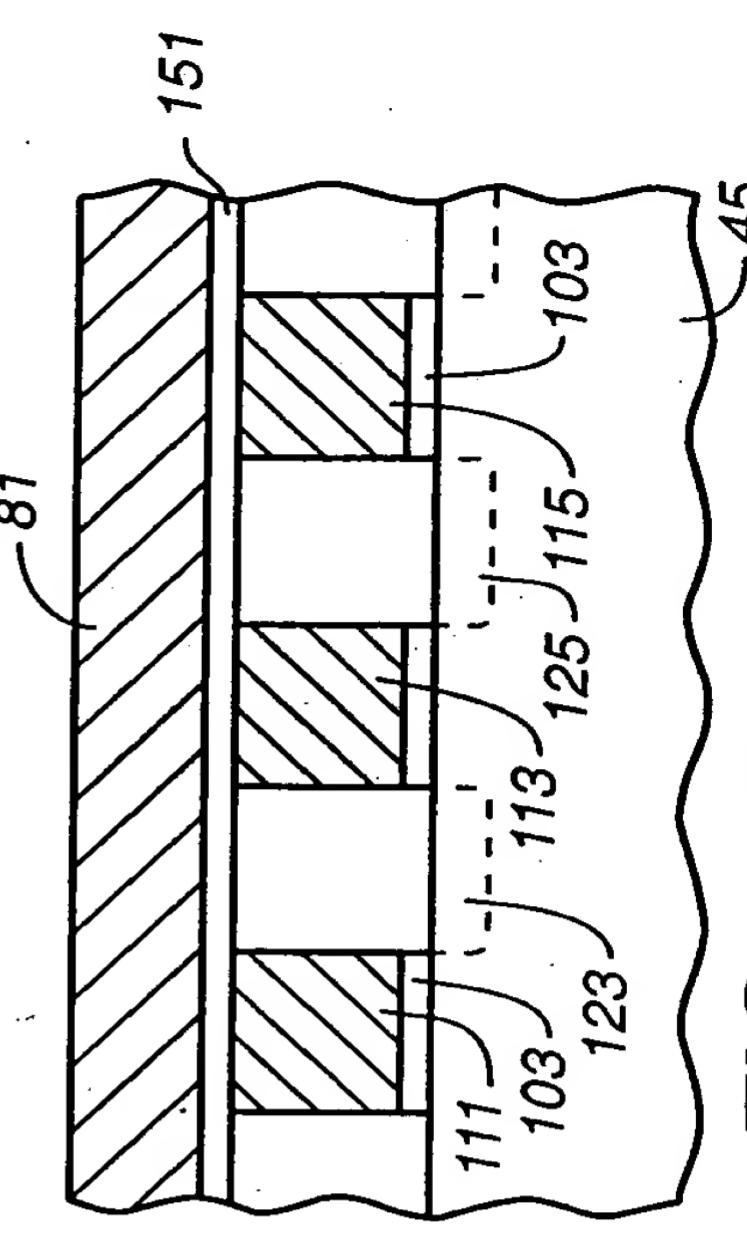


FIG. 8B



Appln. No.: 09/925,102 Docket No.: SNDK.257US0
Title: Scalable Self-Aligned Dual Floating Gate Memory...
Inventors: Yuan et al. Conf. No.: 3186
Filing Date: 8/8/01 Atty. Tel: (415) 318-1160 Sheet 5 of 12

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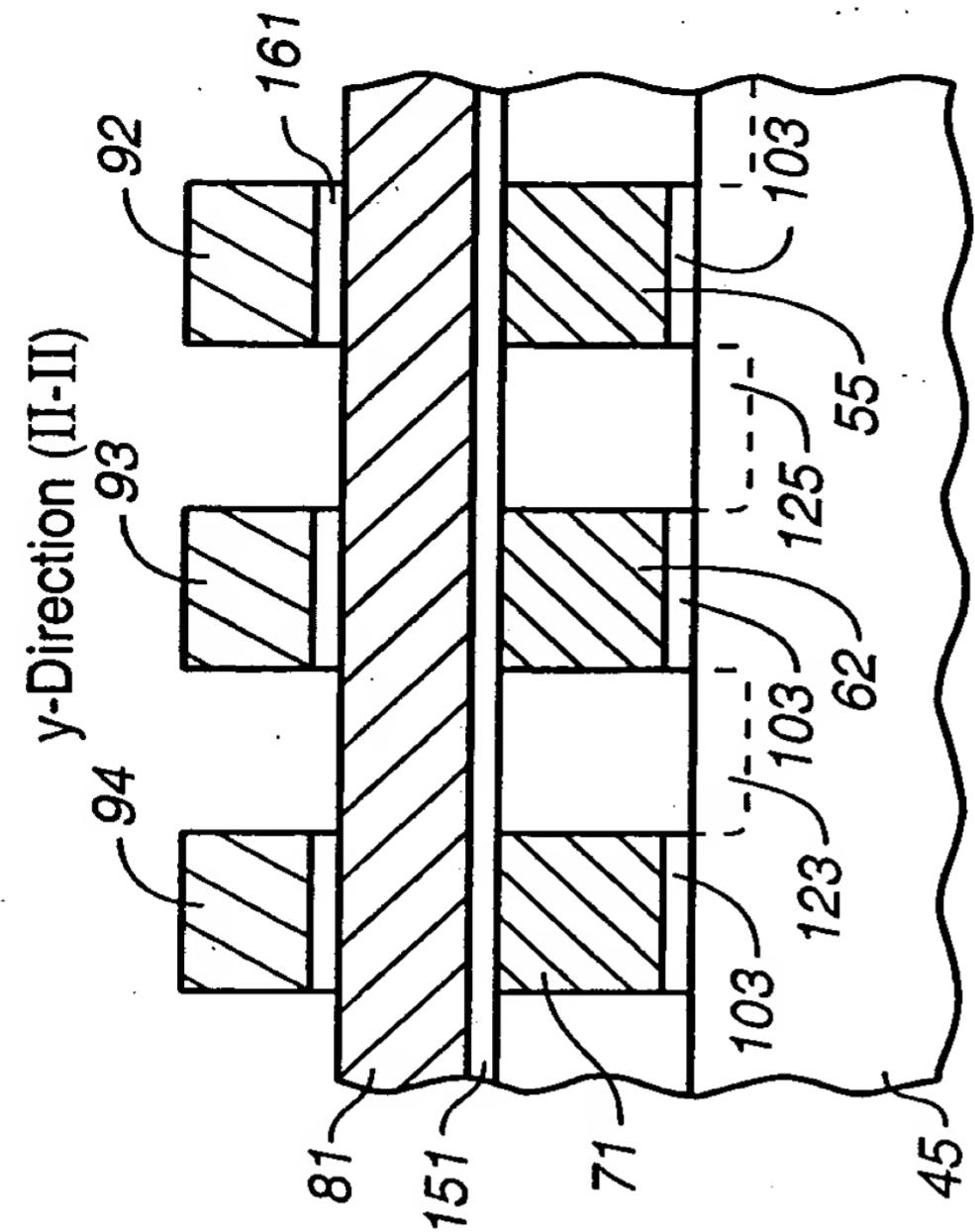


FIG.—9B

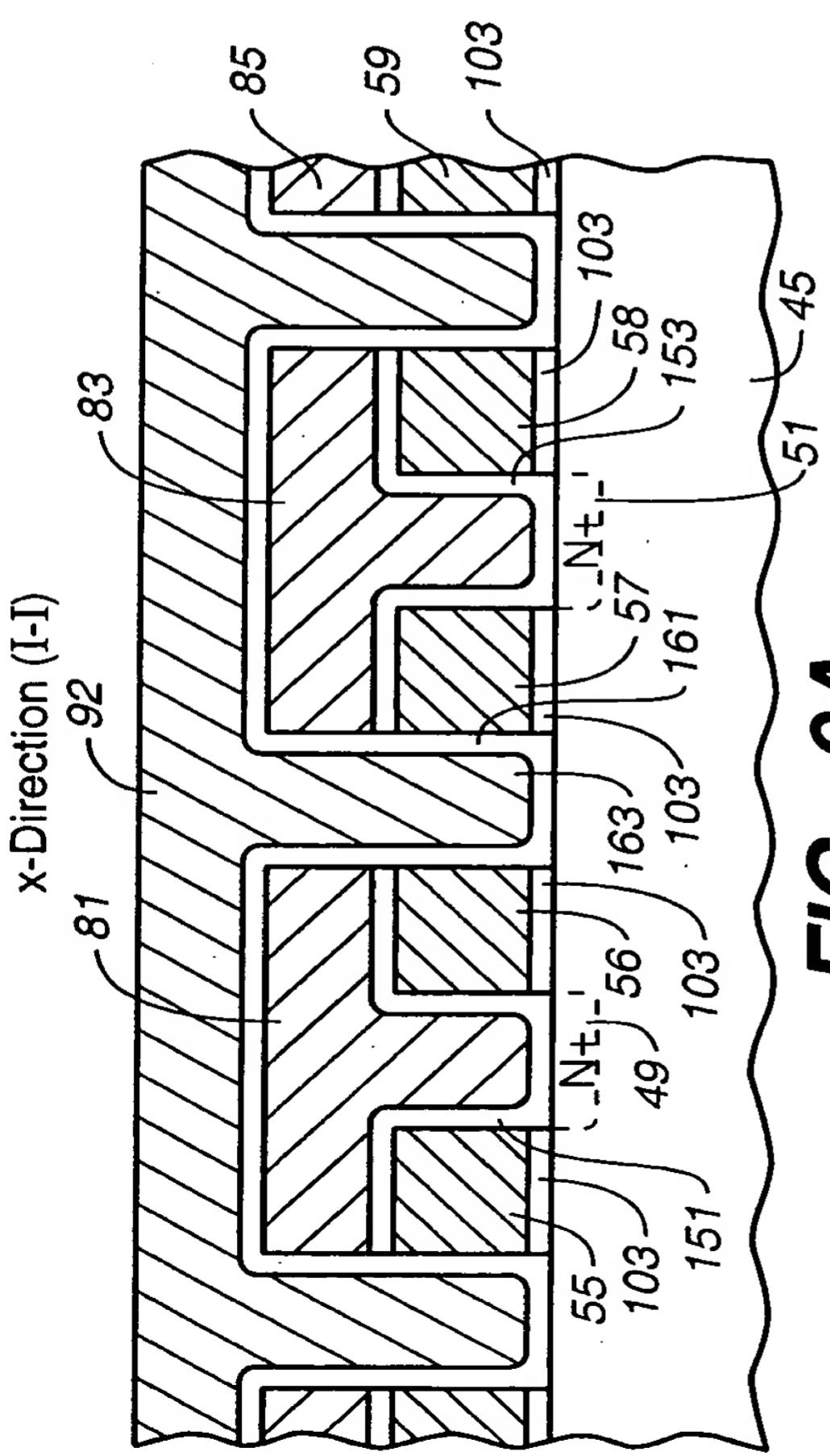


FIG.- 9A

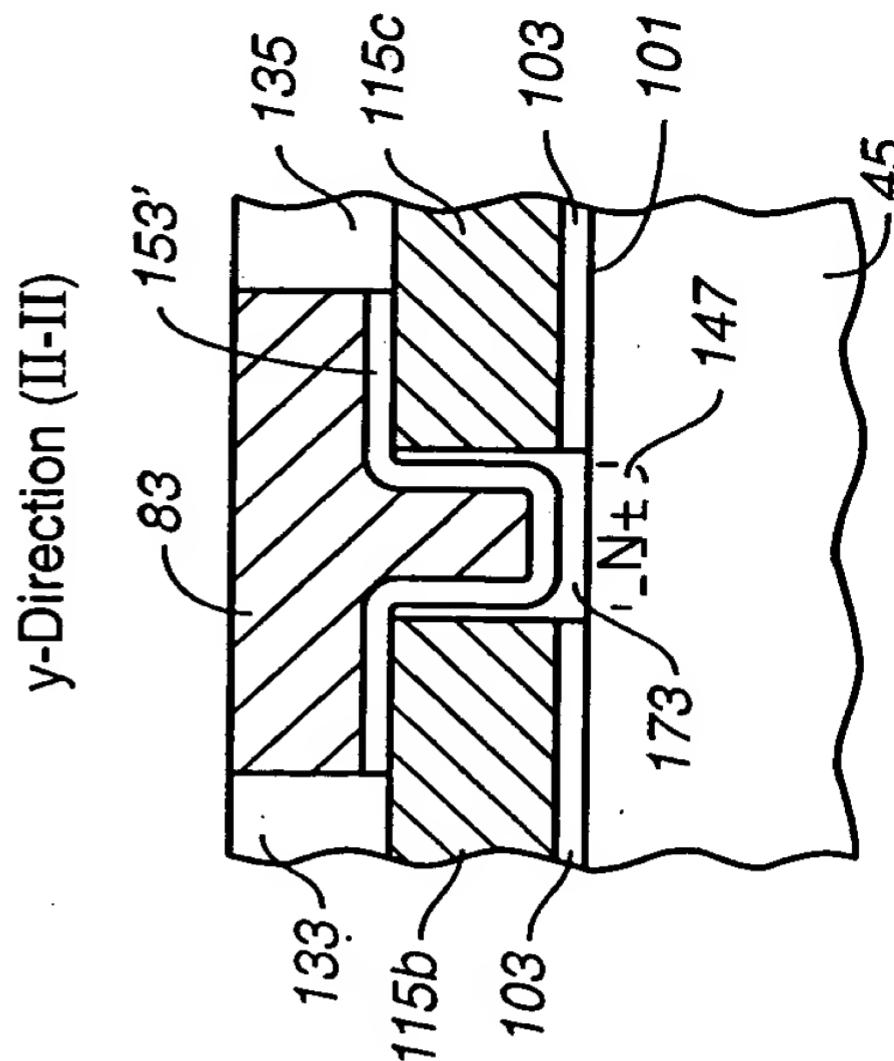


FIGURE 11

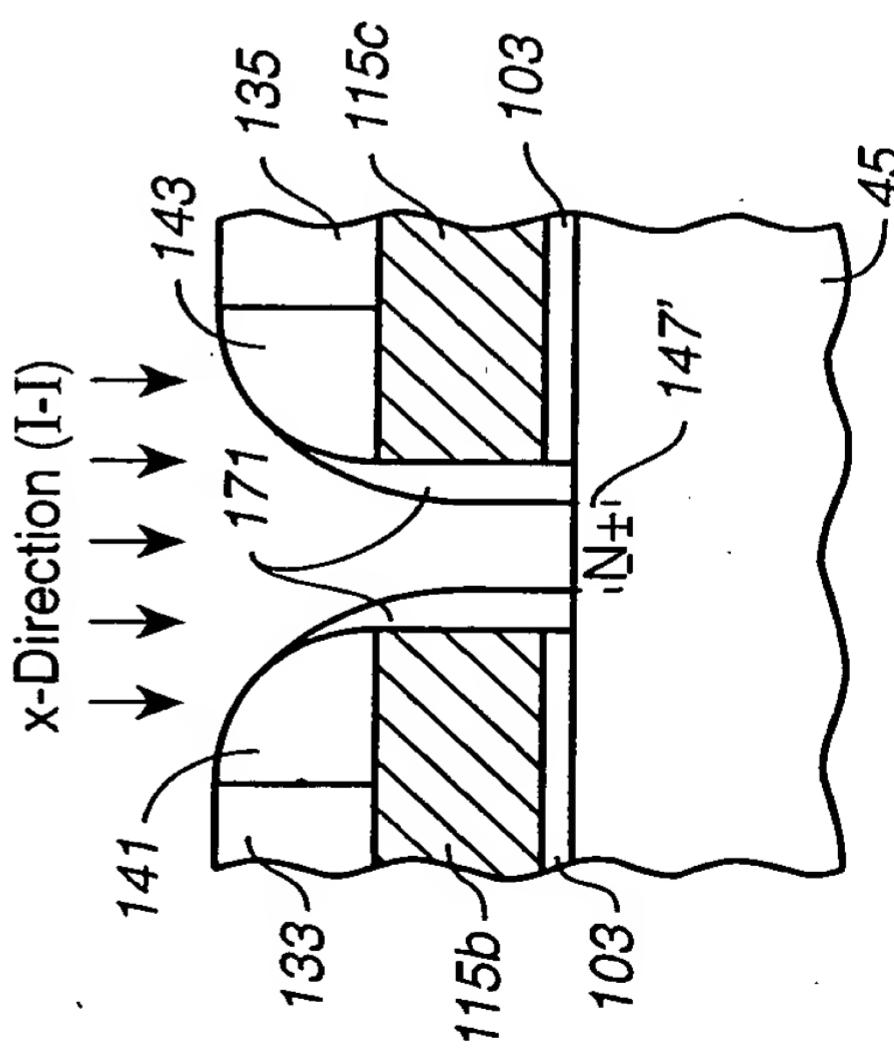


FIG. - 10



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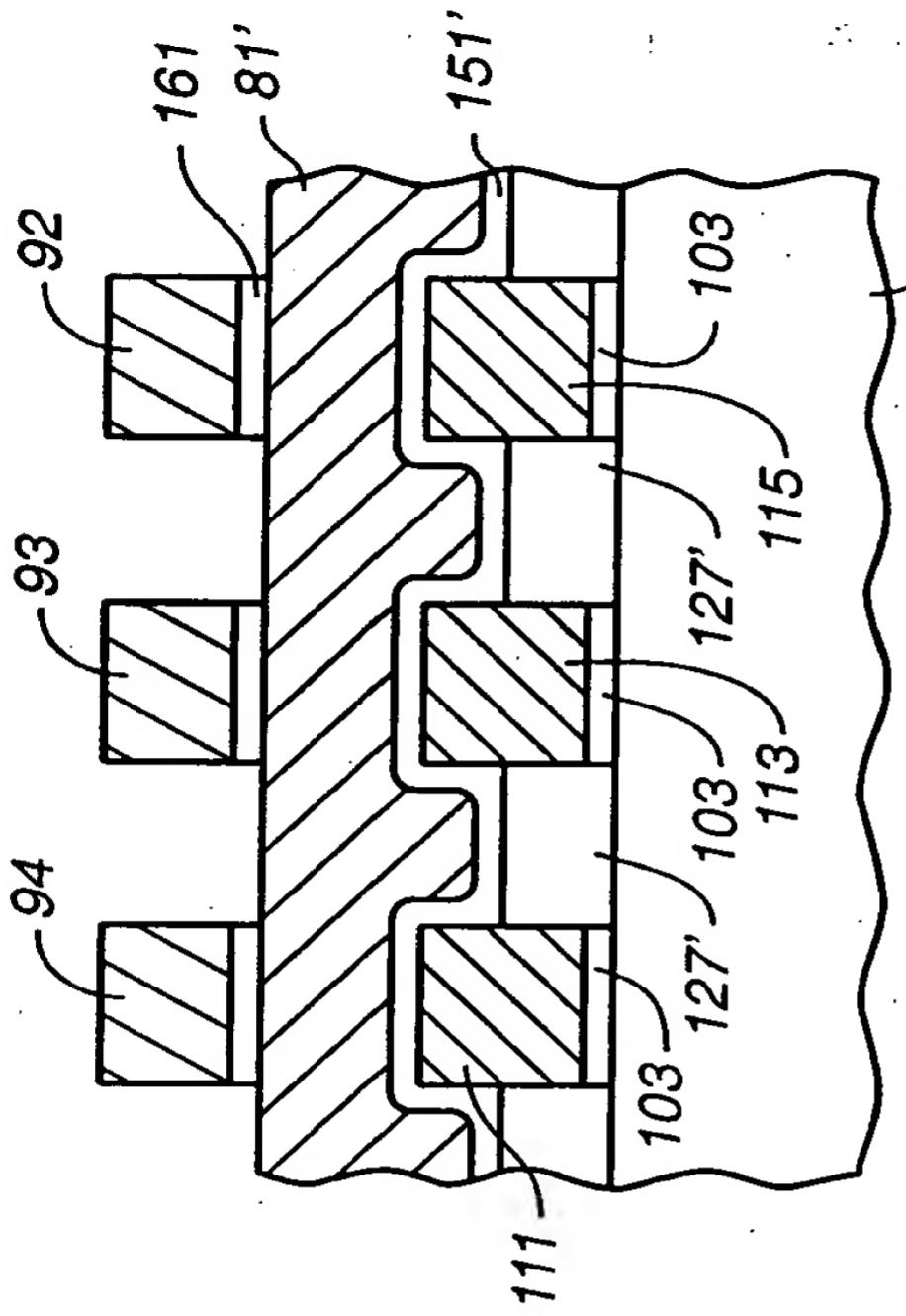


FIG. 12B

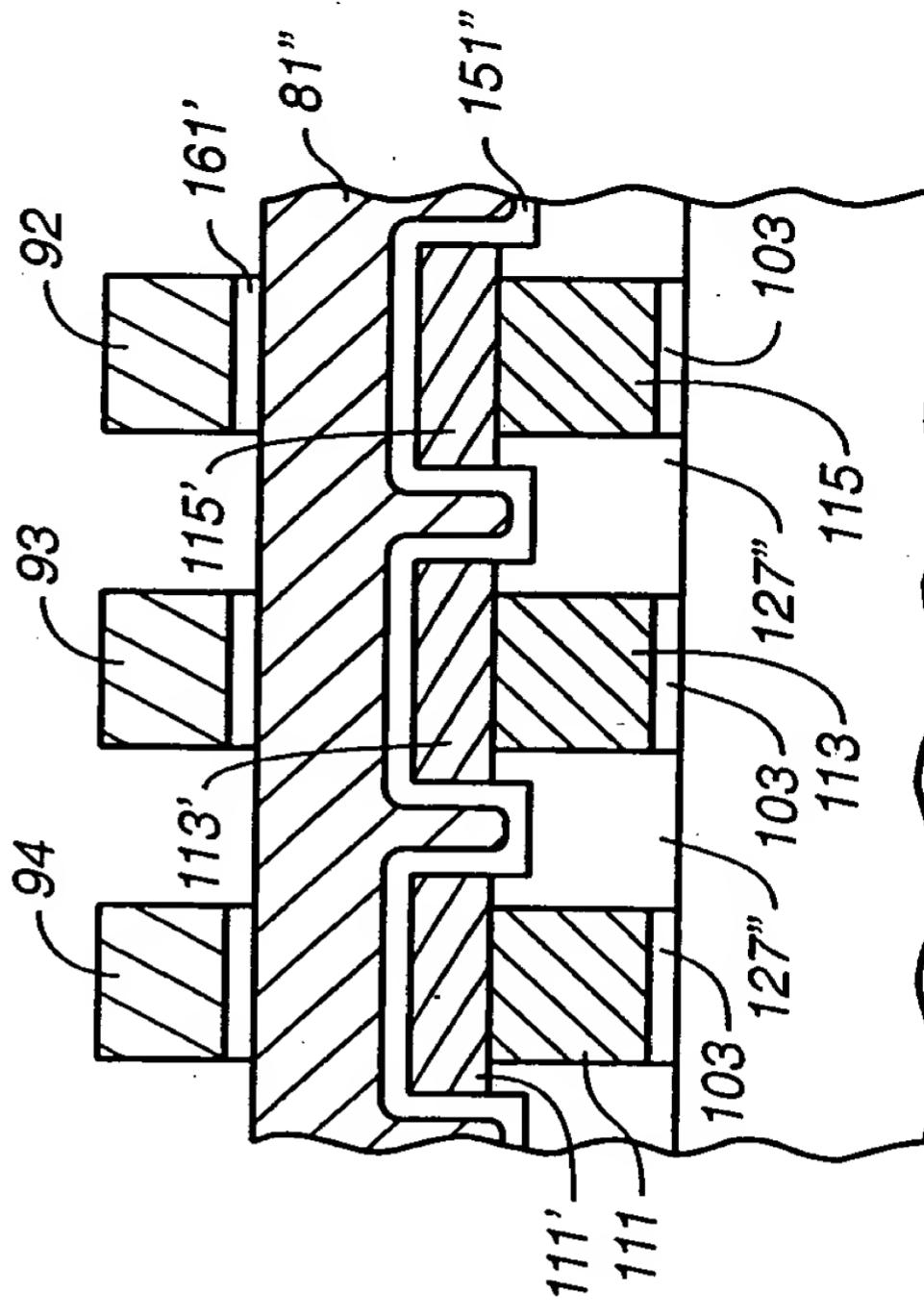


FIG. 13B

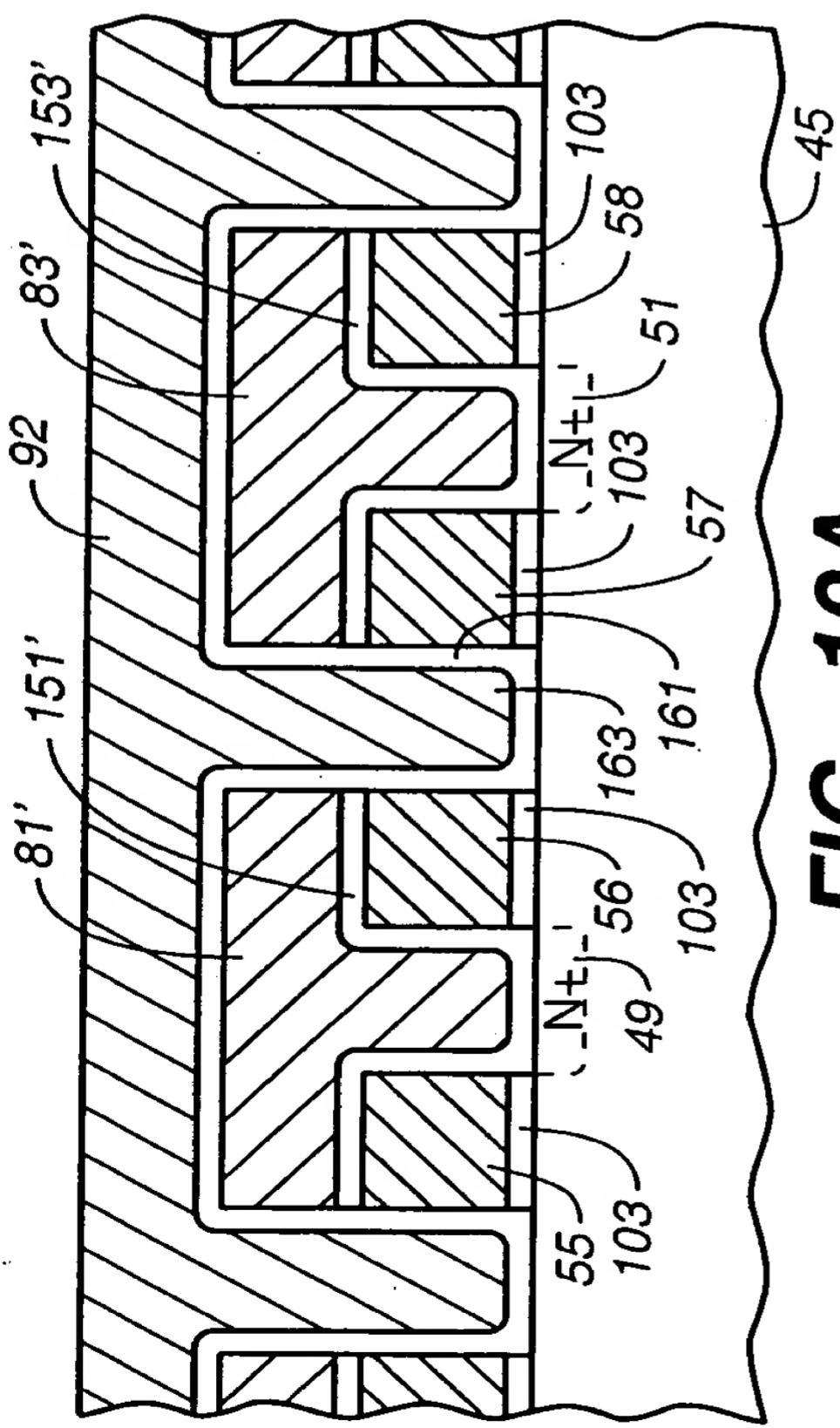


FIG. 12A

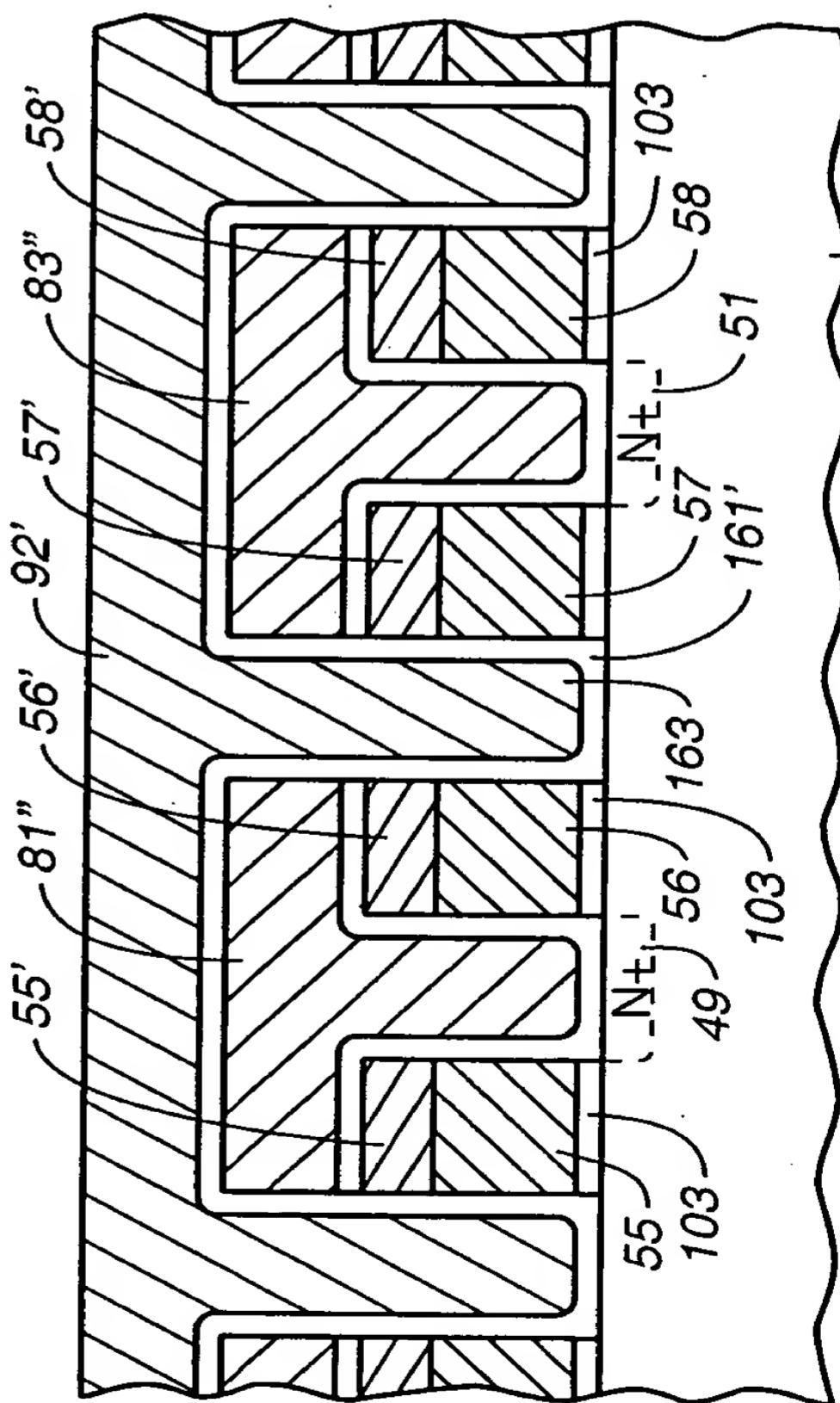


FIG. 13A



Appln. No.: 09/925,102 Docket No.: SNDK.257US0
 Title: Scalable Self-Aligned Dual Floating Gate Memory...
 Inventors: Yuan et al. Conf. No.: 3186
 Filing Date: 8/8/01 Atty. Tel: (415) 318-1160 Sheet 7 of 12

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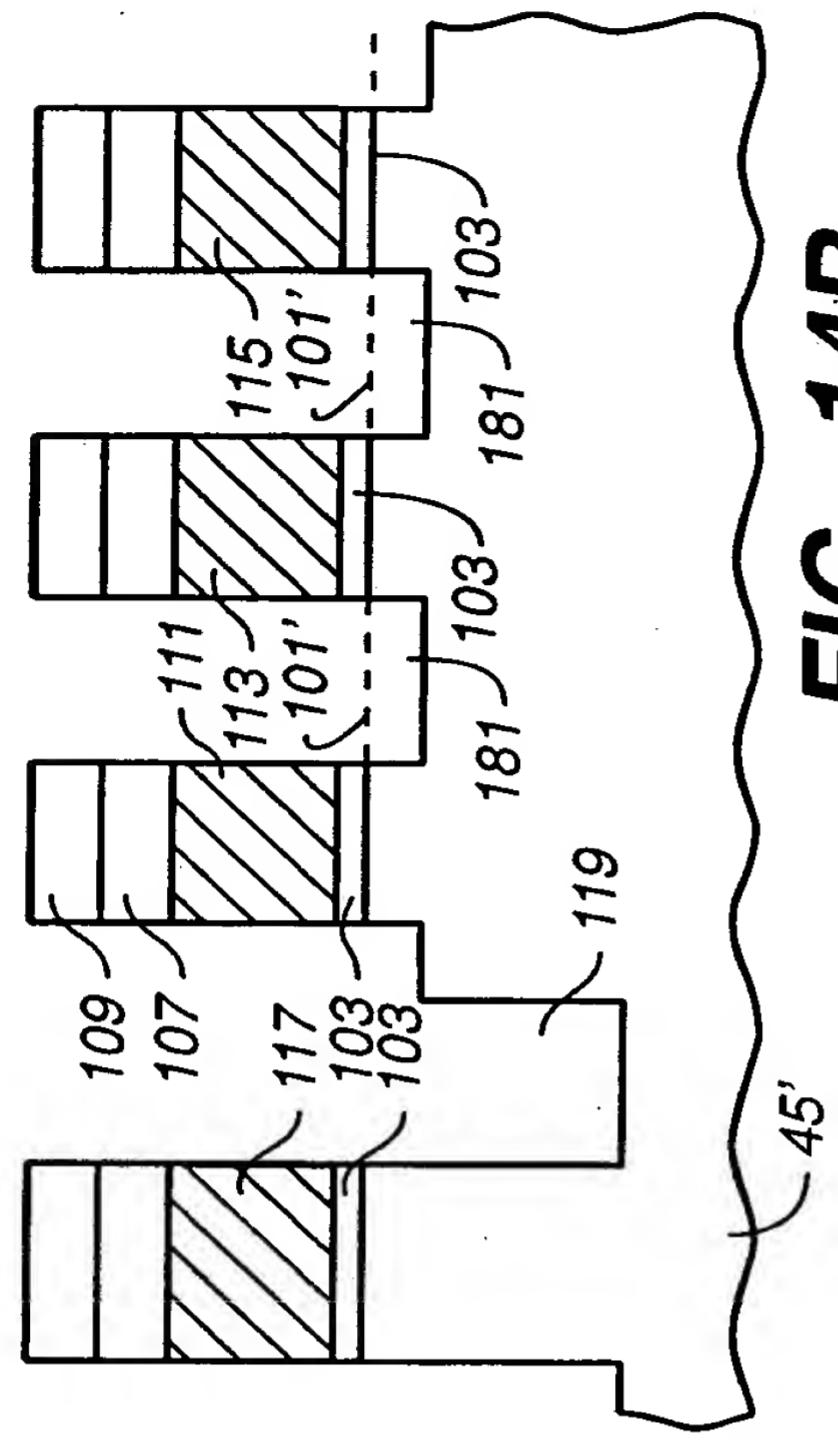


FIG. 14A

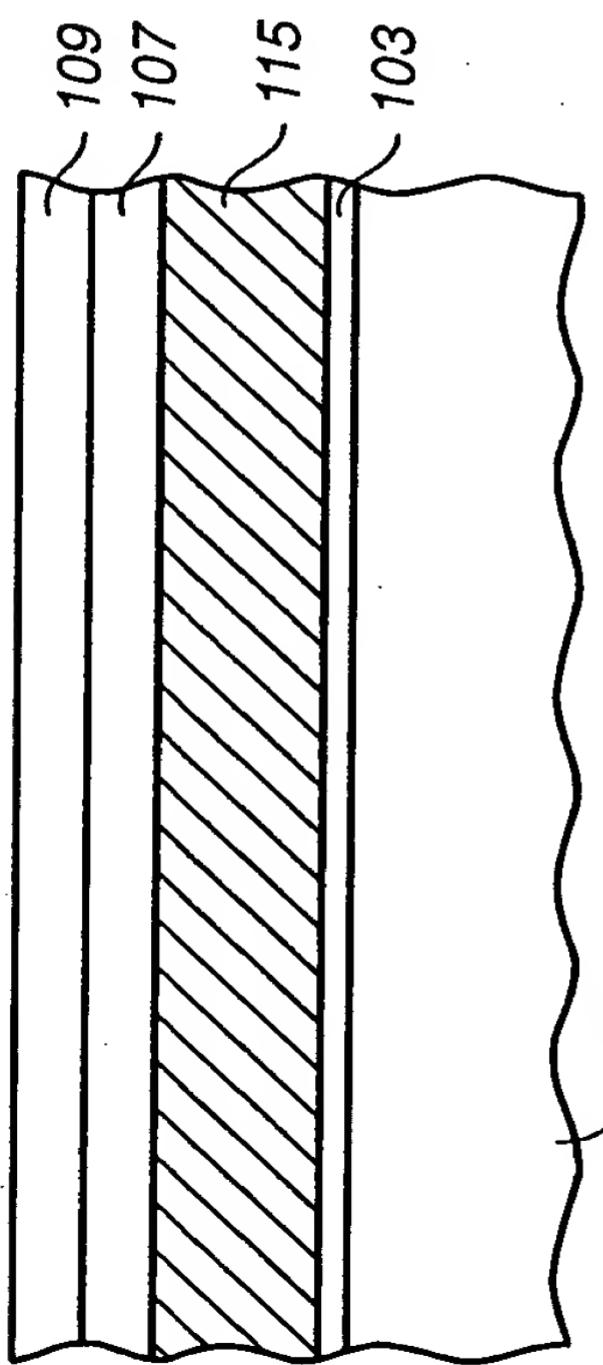


FIG. 14B

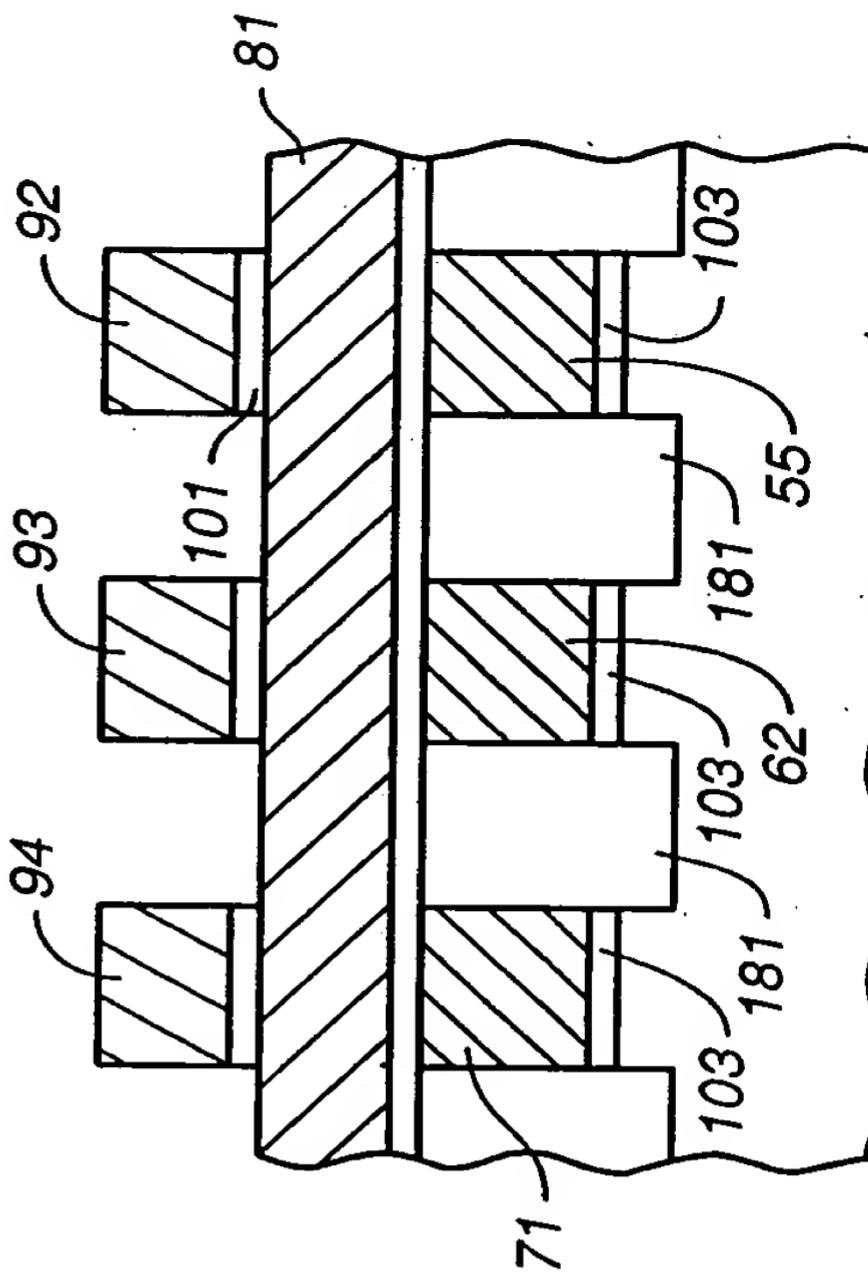


FIG. 15A

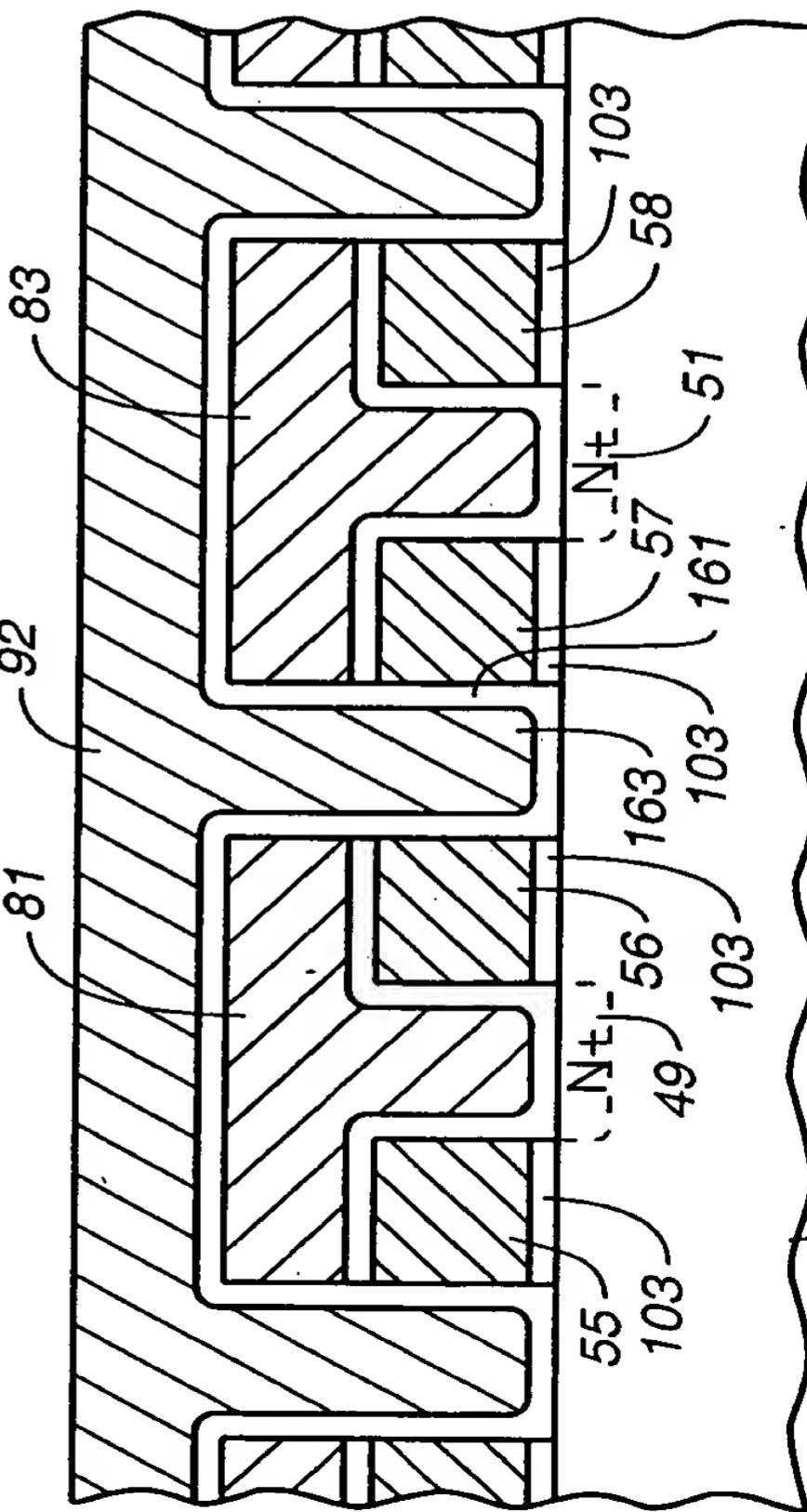


FIG. 15B



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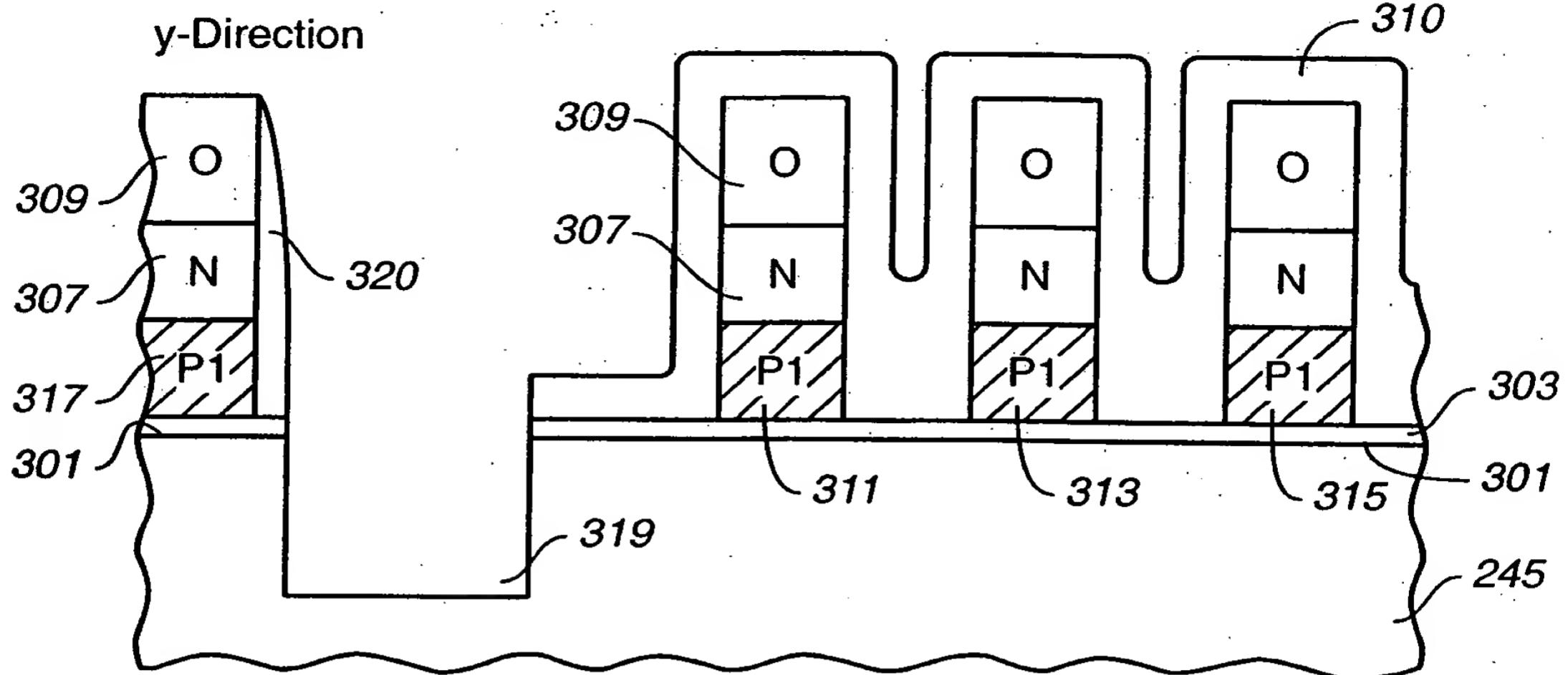


FIG._16

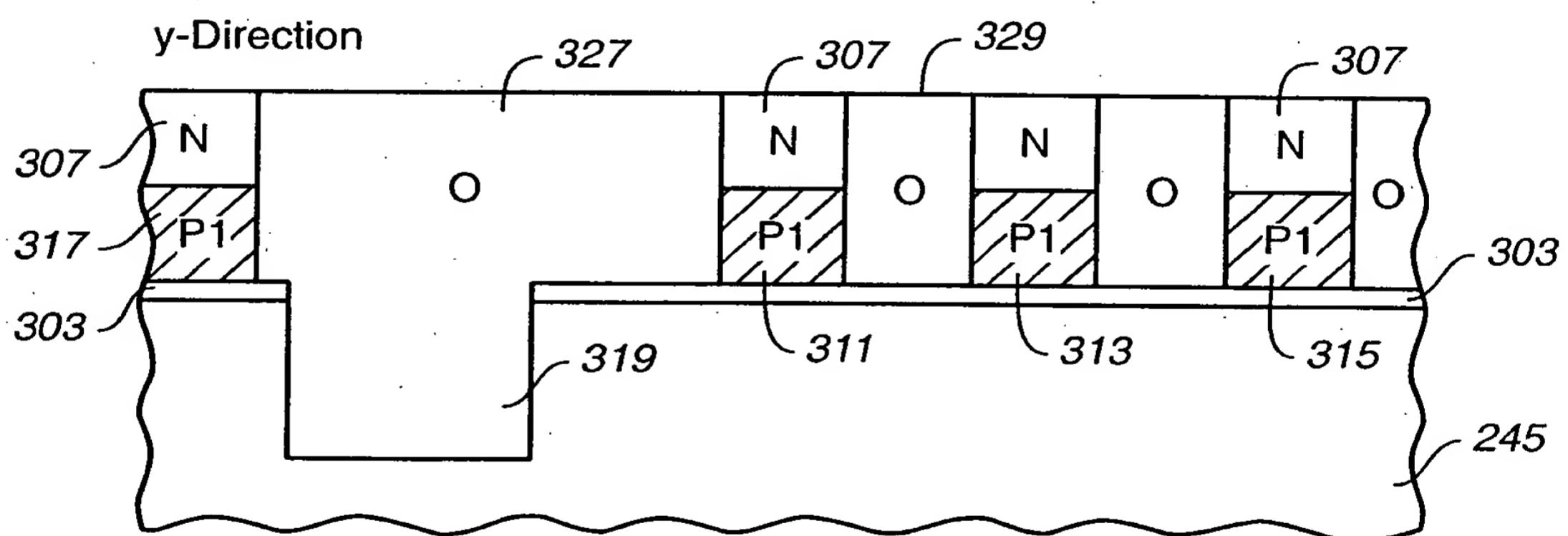


FIG._17

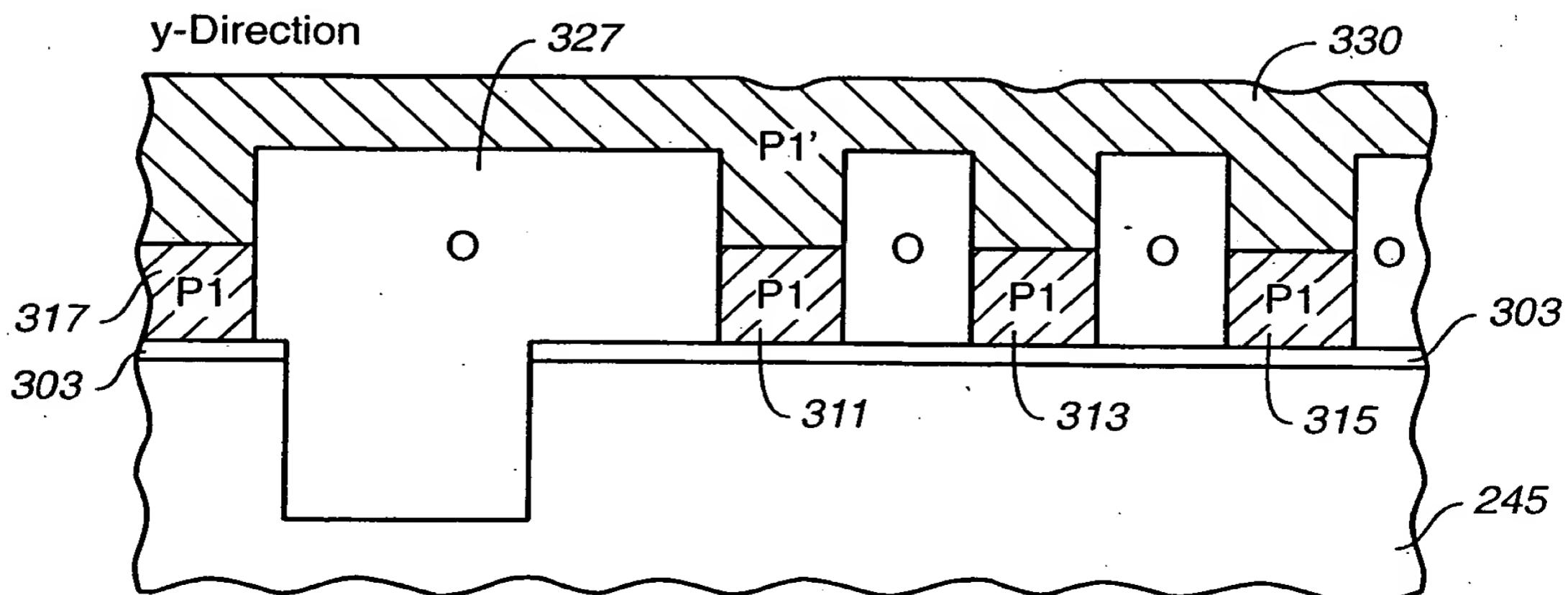


FIG._18

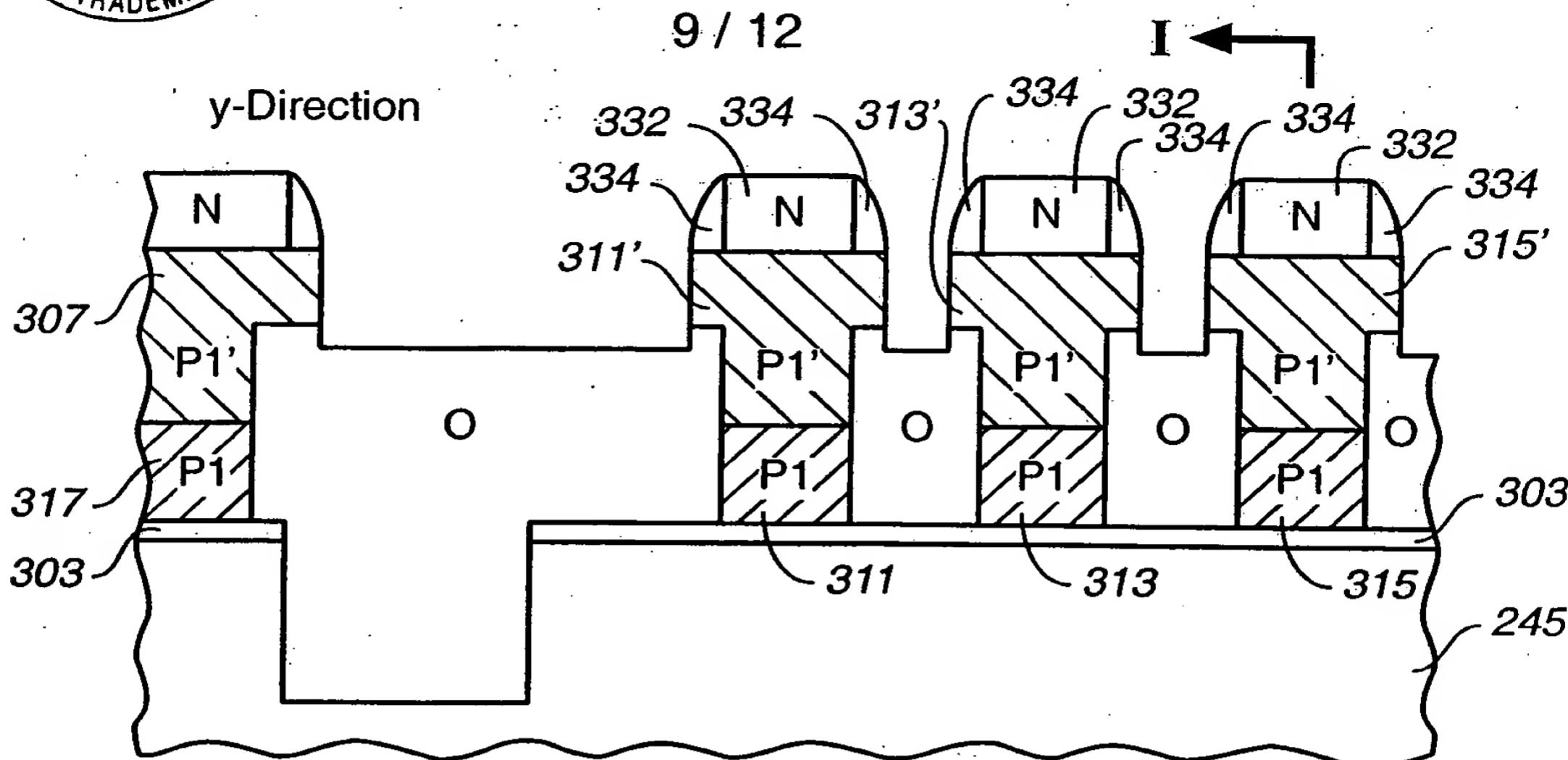


FIG. 19

I ↙

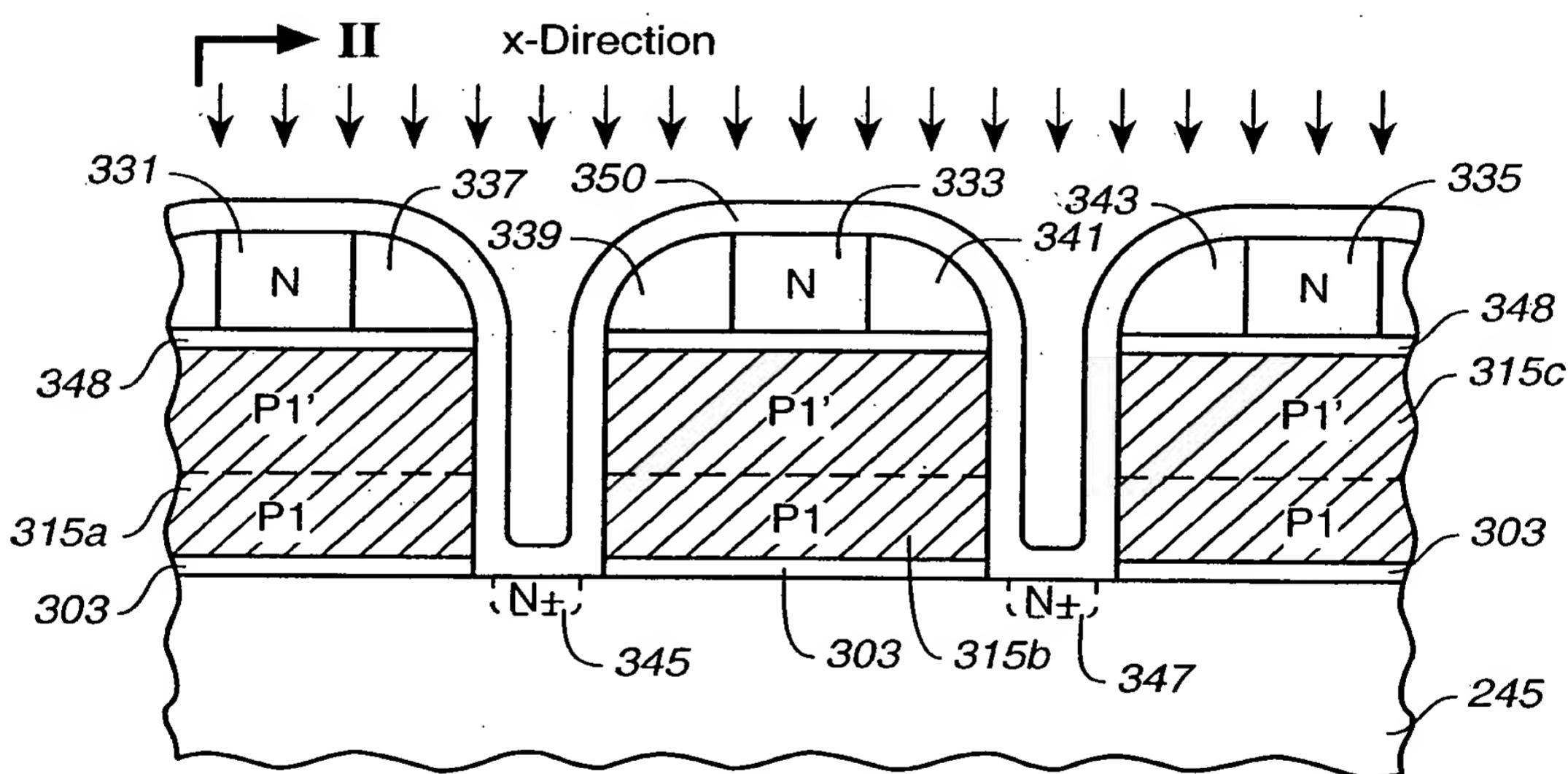


FIG. 20

II ↗

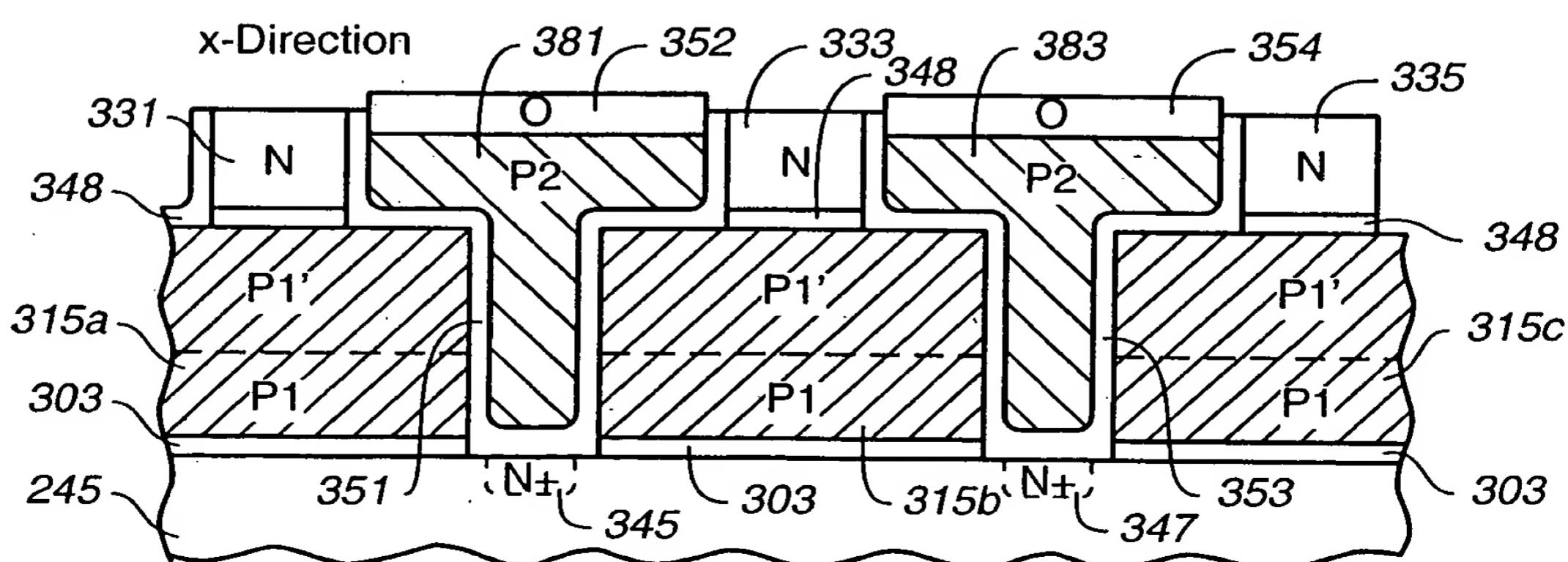


FIG. 21

+



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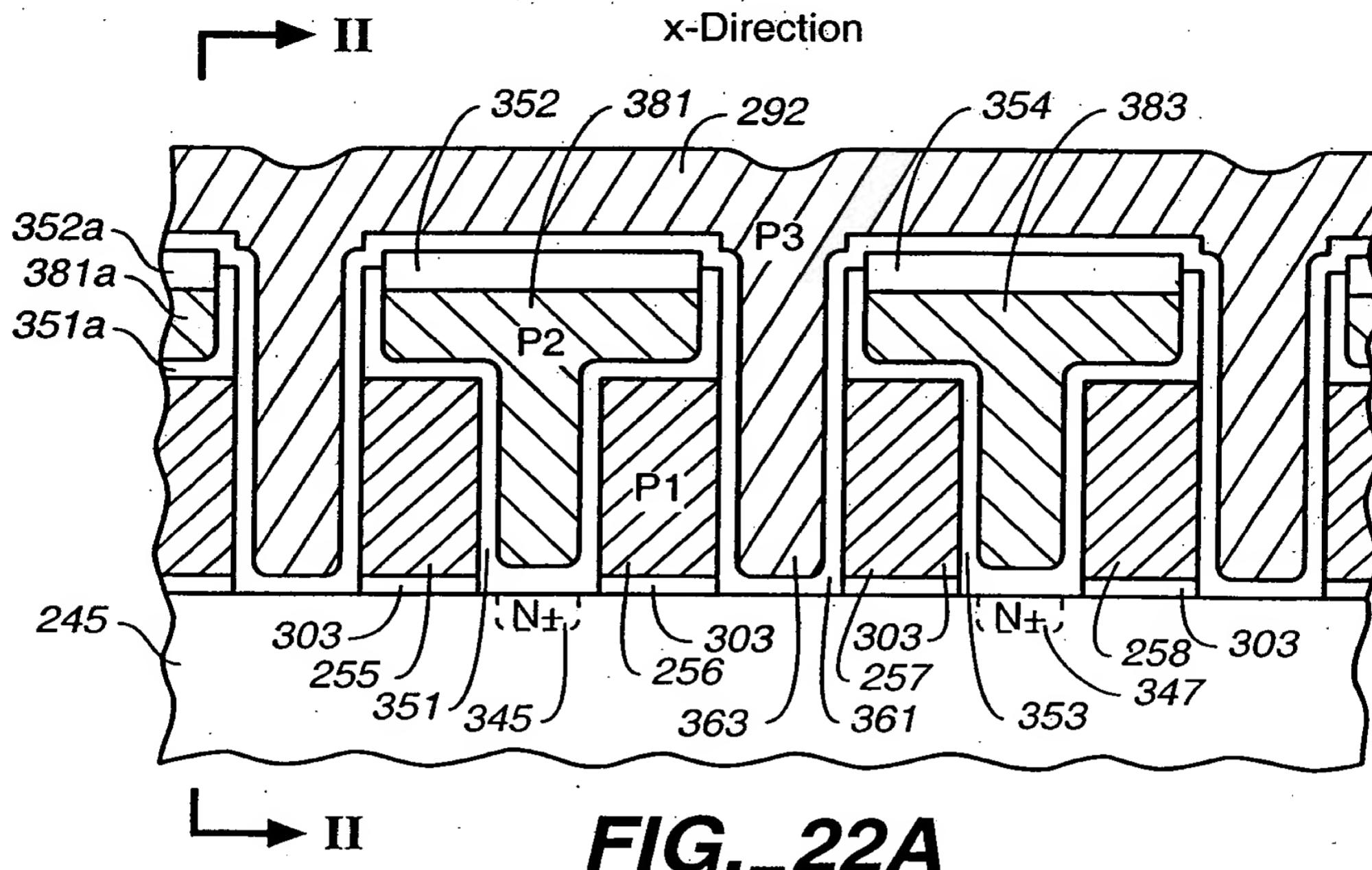


FIG._22A

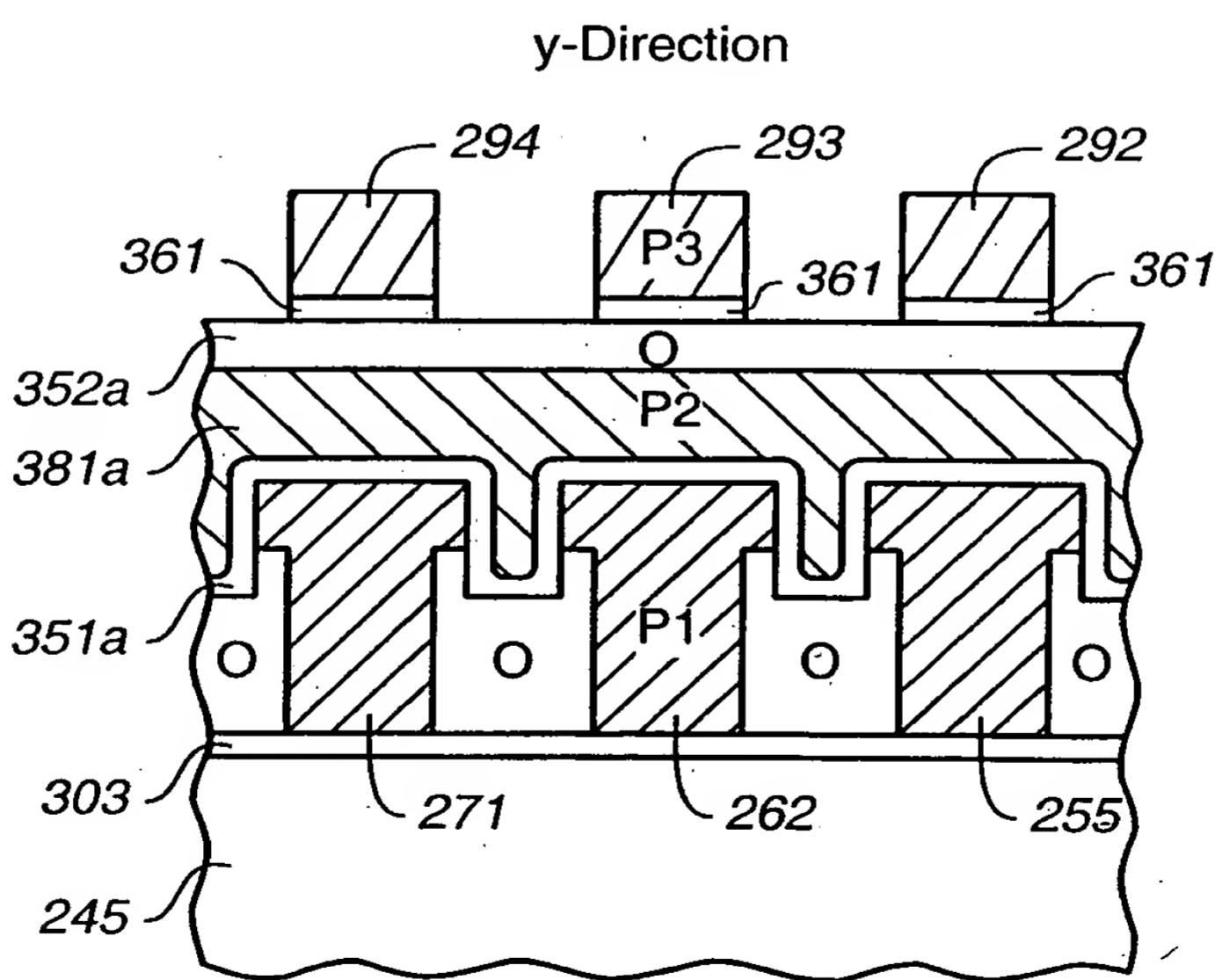


FIG._22B



Section III-II

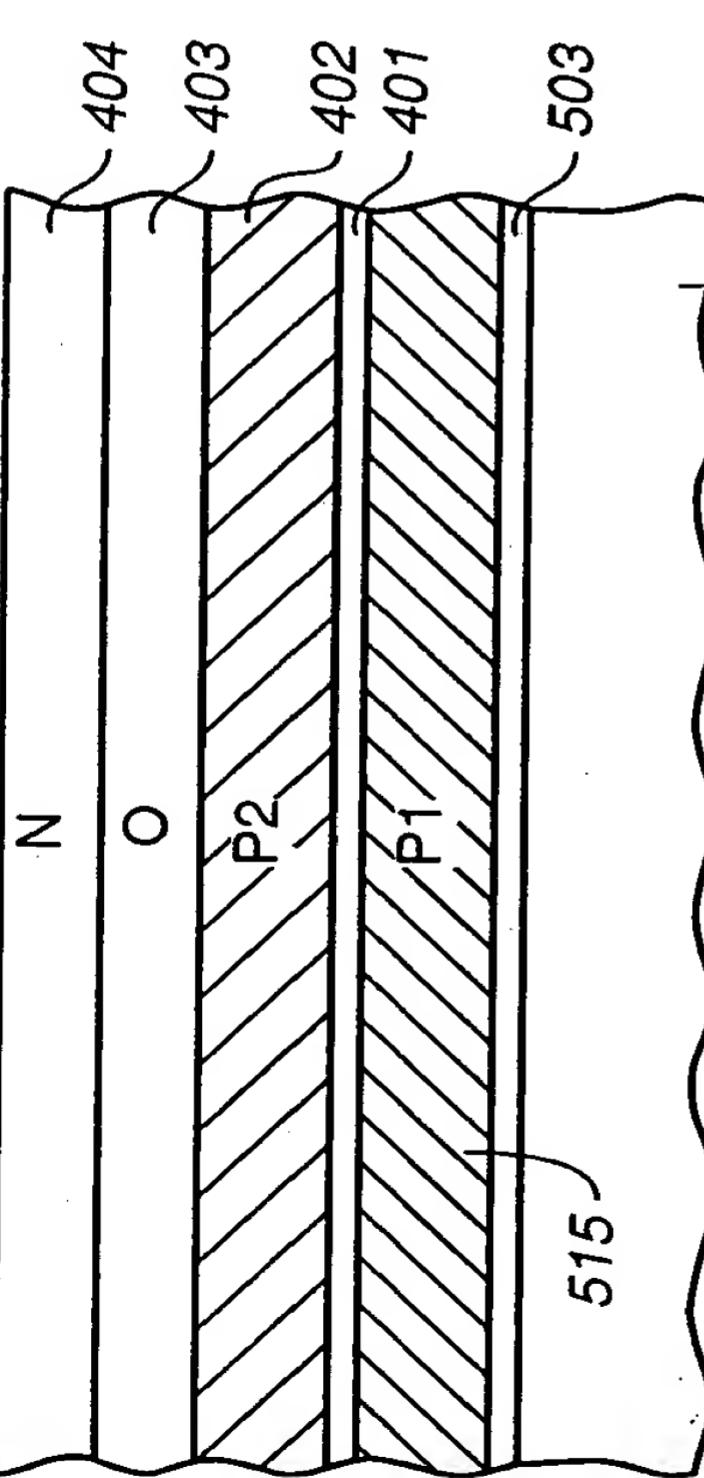


FIG.-23A

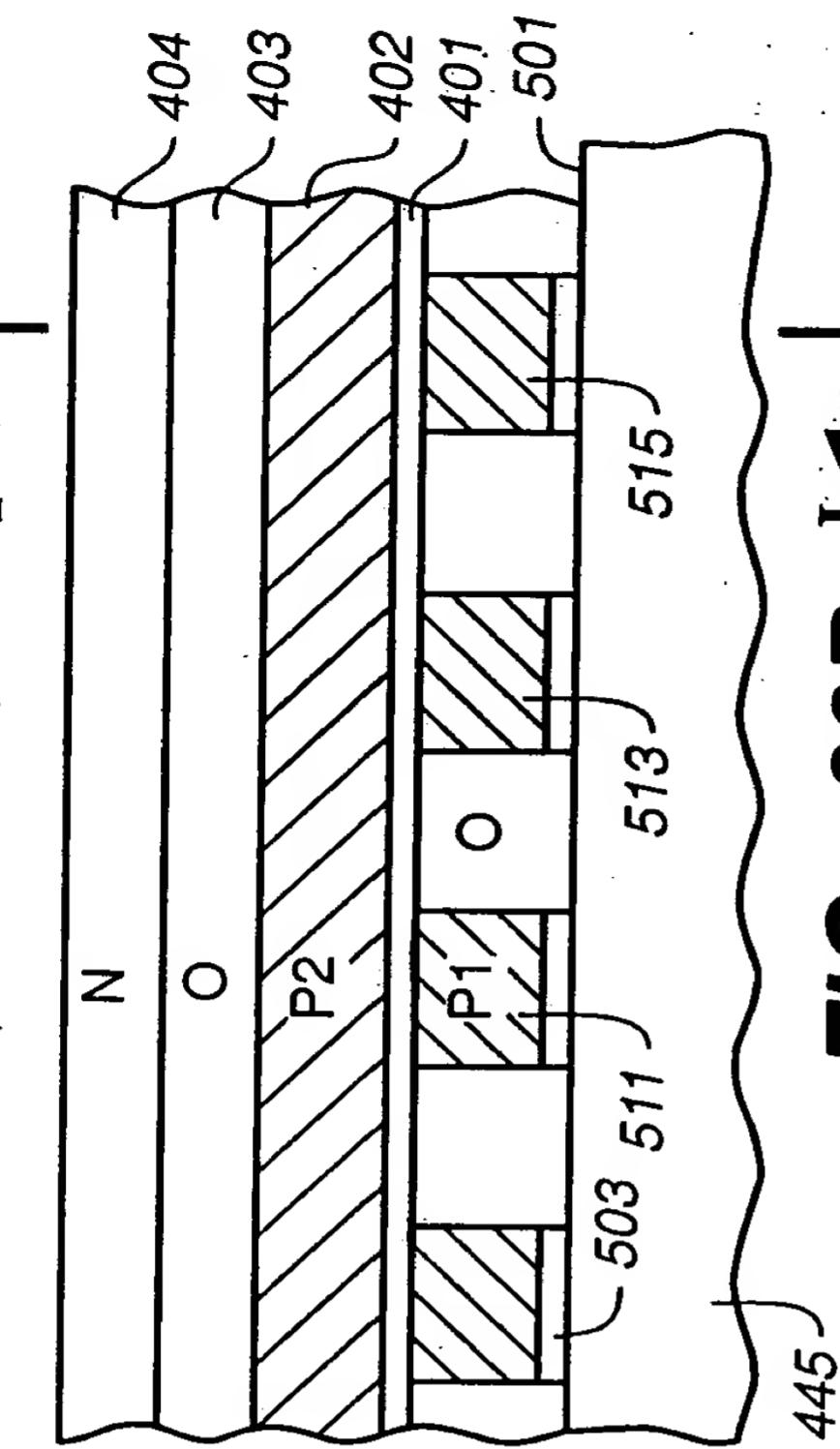


FIG. - 23B

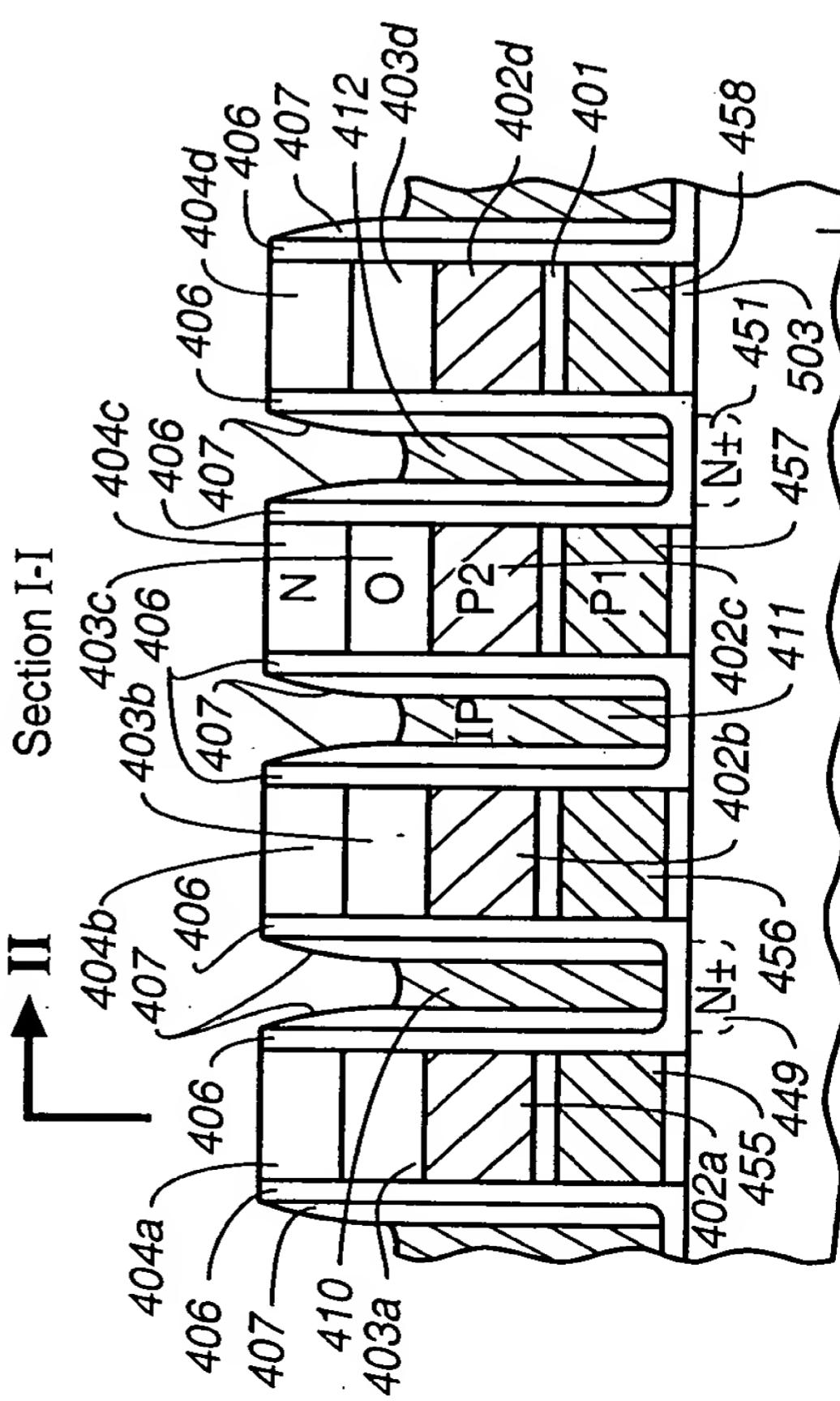


FIG. 24A

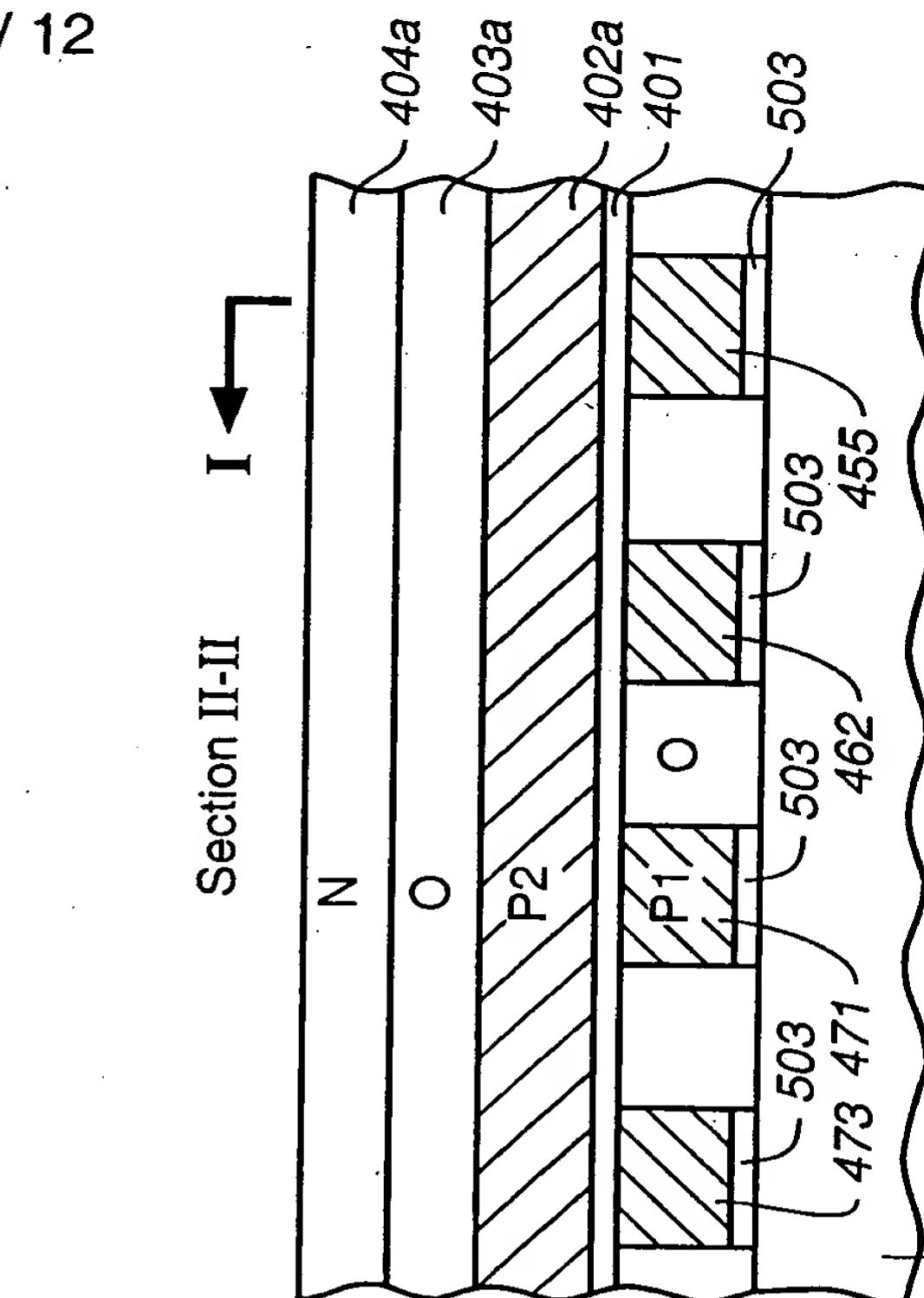


FIG. 24B



Section II-II

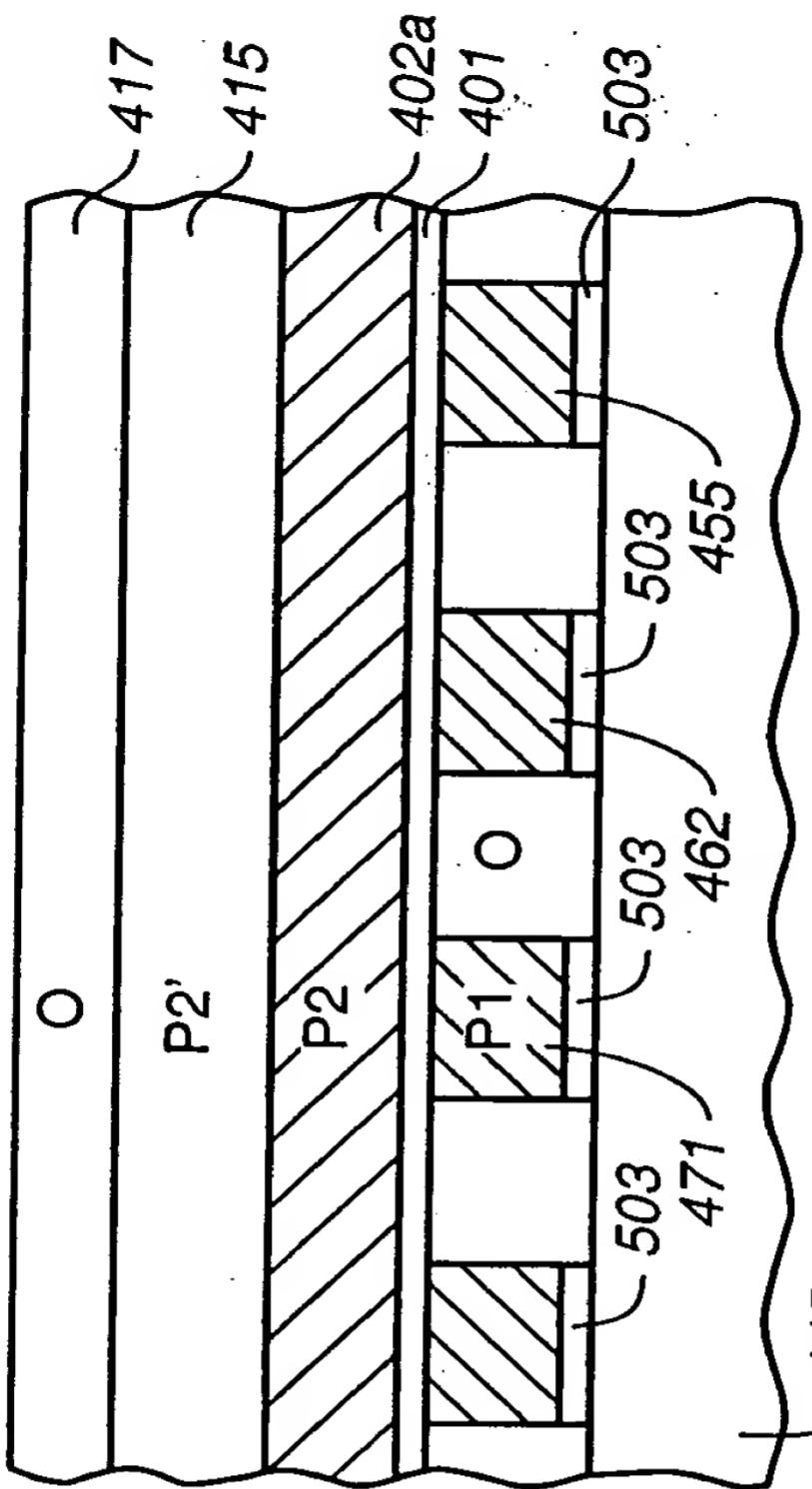


FIG. - 25B

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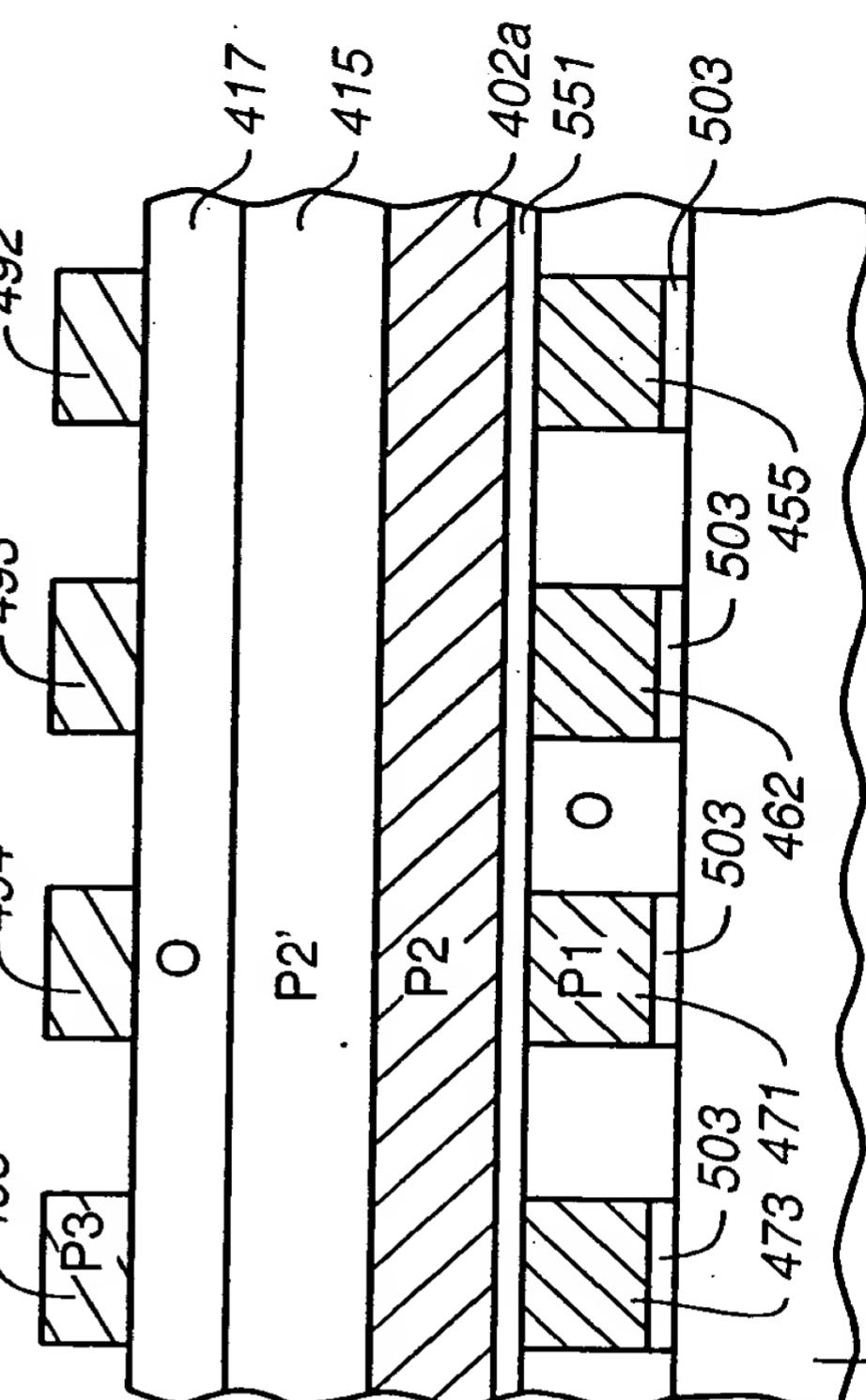


FIG. - 26B

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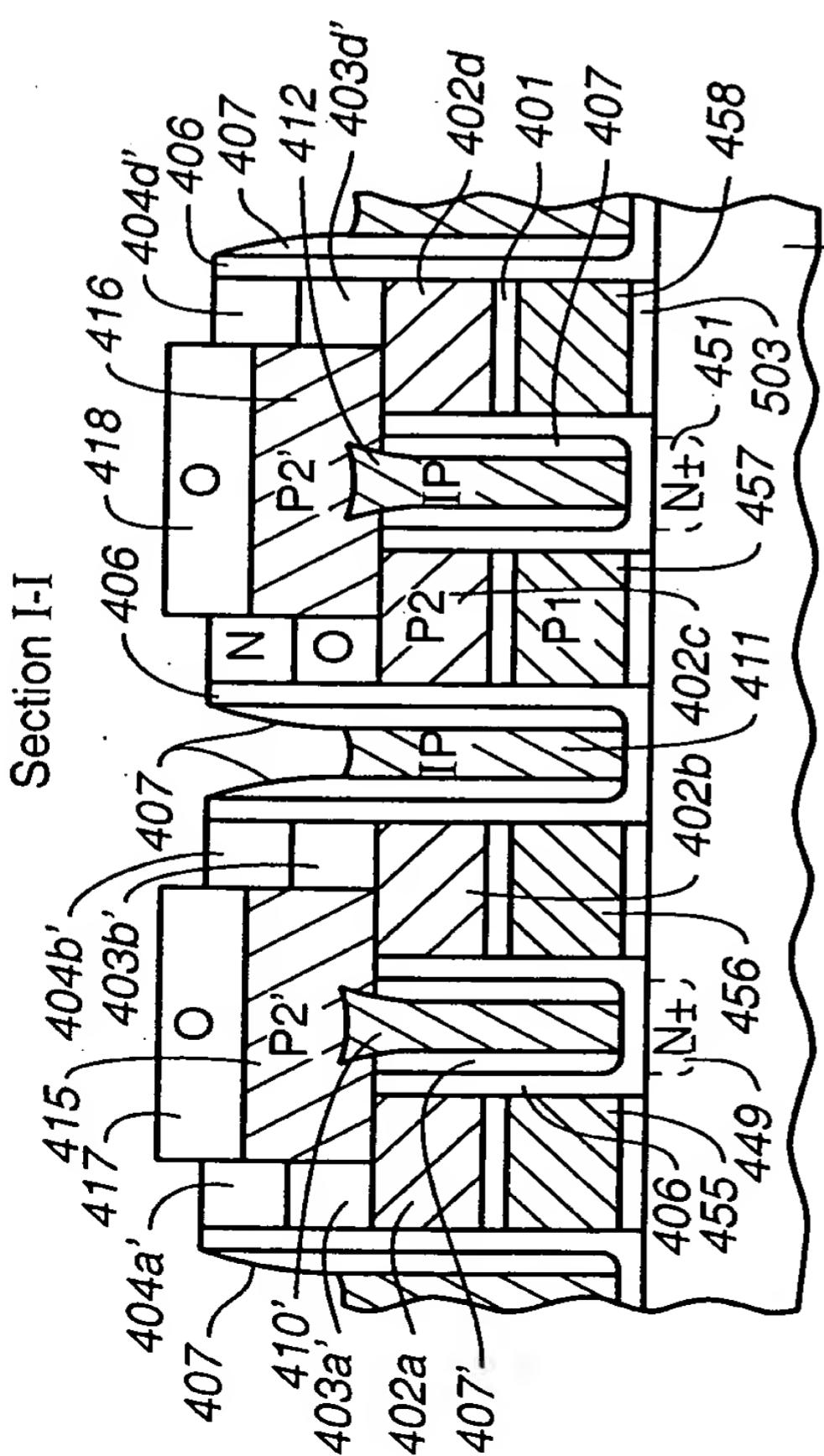


FIG. - 25A 445

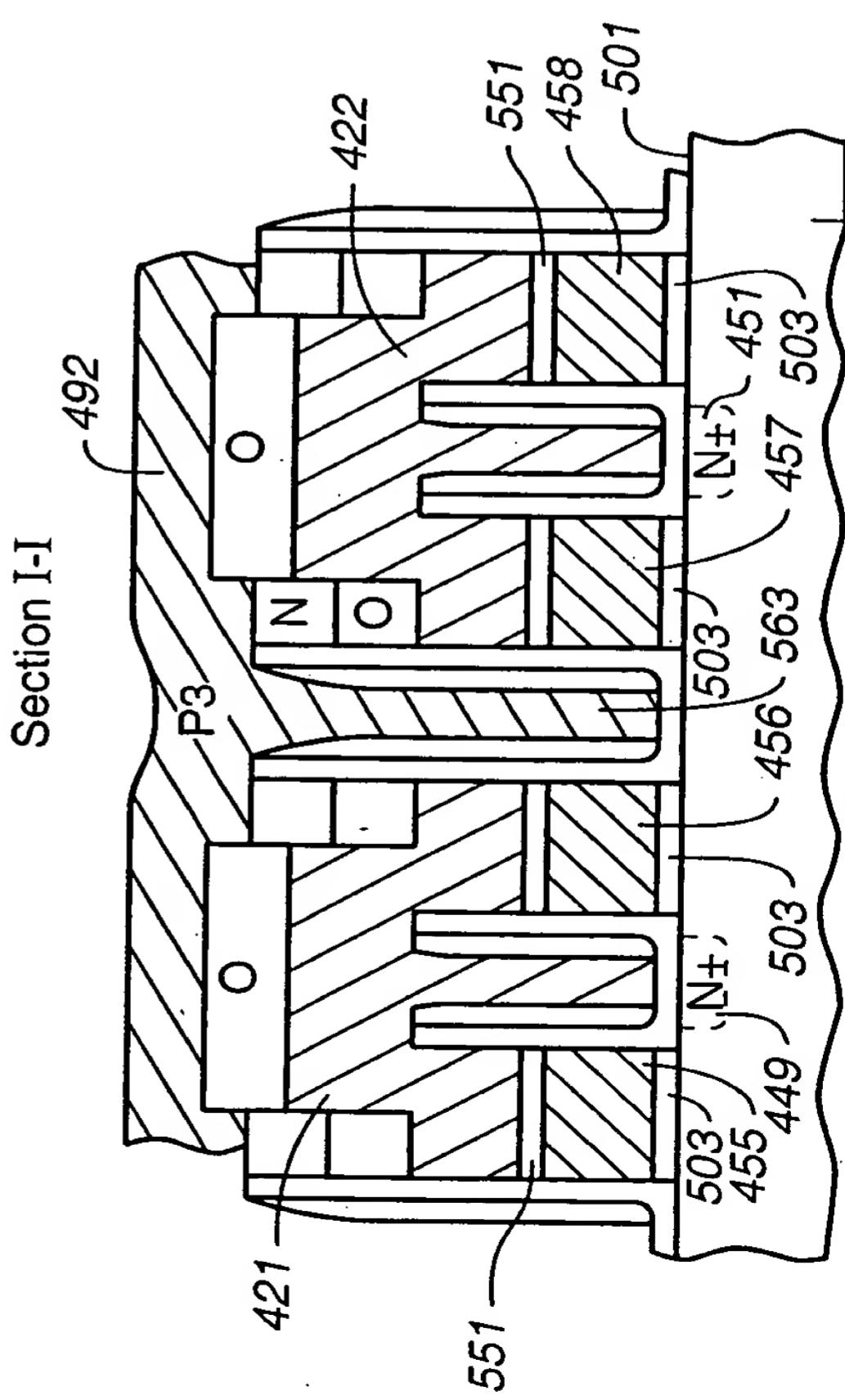


FIG. - 26A 445